

09/07/2004 **Test SPS ionization chambers (ISR model) with Csi source**

Beam Loss electronics setting: capacitor 4700pF, Gain =16, Ceq. = 294 pC

Acquisition time: 9120 ms

ADC resolution 1 bit = 4.882mV

Resolution : Coulomb/bit = 1.434 pC/bit, Current 157.4 fA/bit ( T=9.12 s)

High voltage +800V

Chamber	No source bit	With source bit	Signals bit	Current out A
1	2	290	288	4.53E-11
2	1	304	303	4.76E-11
3	1	298	297	4.67E-11
4	4	295	291	4.58E-11
5	2	304	302	4.75E-11
6	4	304	300	4.72E-11
7	2	296	294	4.62E-11
8	3	304	301	4.73E-11
9	2	293	291	4.58E-11
10	1	297	296	4.65E-11
			averd	4.66E-11
			max	4.76E-11
			min	4.53E-11
			std	8.19E-13
			%pp	5.06