

**MEMORY MAPPING BLECS**  
MERGE 700000

pDev-&gt;bIcCsHVLFRESSETUP1.set((const long int \*)pAc-&gt;bIcCsHVLFRESSETUP1,256);

Name	Alternative name	Database-name	Page	Start	End	Bit position	data type	Bit field	struct entry (automatic)	FESA Field	Property DataFieldRef	Property DataFieldRef Log	struct to Fesa Field	Data to field (not logged)	BLECSFlash	BLMSystemCheck	IMConsistencyMCSCheck	BLMBisLinkCheck	DWORD Description	Comments
<b>LOGGING</b>																				
SYS_OP	SysOperational	SYS_OPERATIONAL	OVERVIEW	700000	700004	31	bool	:1	bool bIcCsSYS_OP;1;	name="bIcCsSYS_OP"	field-ref-item field-	>bIcCsSYS_OP.set(p						System level	System is operational (View by the BLECS)	
TEST	SysUnderTest	SYS_UNDER_TEST	OVERVIEW			30	bool	:1	bool bIcCsTEST;1;	name="bIcCsTEST" multiplexing-	field-ref-item field-	>bIcCsTEST.set(pAc-				field-ref-item field-	field-ref-item field-	field-ref-item field-	System is under test (All BLM crate of the IP are involved but only this crate is tested)	
T_REQ	TsReq	TEST_PEND_GLOBAL	OVERVIEW			29	bool	:1	bool bIcCsT_REQ;1;	name="bIcCsT_REQ" multiplexing-	field-ref-item field-	>bIcCsT_REQ.set(pAc-							Test requested (while take place when the conditions are fulfill)	
FIRM_SERIAL_FAULT	Firmware serial fault	FIRM_SERIAL_FAULT	OVERVIEW			28	bool	:1	bool bIcCsFIRM_SERIAL_FAULT;1;	name="bIcCsFIRM_SERIAL_FAULT"	field-ref-item field-	>bIcCsFIRM_SERIAL_							Mismatch between flash setup and FPGA firmware or board serial number	
HV1	FaultHighV1Comp	HV_1_COMP_FAULT	OVERVIEW			27	bool	:1	bool bIcCsHV1;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsHV1.set(pAc-							High voltage 1 fault (survey by comparators)	
HV2	FaultHighV2Comp	HV_2_COMP_FAULT	OVERVIEW			26	bool	:1	bool bIcCsHV2;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsHV2.set(pAc-							High voltage 2 fault (survey by comparators)	
HV12ADC	FaultHighV12adc	HV_ADC_FAULT	OVERVIEW			25	bool	:1	bool bIcCsHV12ADC;1;	name="bIcCsHV12ADC"	field-ref-item field-	>bIcCsHV12ADC.set(p							High voltage 1 or 2 fault (survey by ADC)	
LVCC	FaultLowVComp	LV_COMP_FAULT	OVERVIEW			24	bool	:1	bool bIcCsLVCC;1;	name="bIcCsLVCC" multiplexing-	field-ref-item field-	>bIcCsLVCC.set(pAc-							Low voltage fault with (survey by comparators)	
LVA	FaultLowVAdc	LV_ADC_FAULT	OVERVIEW			23	bool	:1	bool bIcCsLVA;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsLVA.set(pAc-							Low voltage fault (survey by ADC)	
BE	FaultBmEngy	BEAM_ENGY_FAULT	OVERVIEW			22	bool	:1	bool bIcCsBE;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsBE.set(pAc-							Beam Energy fault (From CIVS card AB/CO)	
TCKF	FaultTurnClk	TURN_CLK_FAULT	OVERVIEW			21	bool	:1	bool bIcCsTCKF;1;	name="bIcCsTCKF" multiplexing-	field-ref-item field-	>bIcCsTCKF.set(pAc-							Turn Clock fault (From BOBR card)	
DUMP	DumpOccurred	DUMP_OCCURRED	OVERVIEW			20	bool	:1	bool bIcCsDUMP;1;	name="bIcCsDUMP" multiplexing-	field-ref-item field-	>bIcCsDUMP.set(pAc-							Dump occurred	
UST	TsPendUsrSysTest	TEST_PEND_USR_SYS_TST	TESTS			19	bool	:1	bool bIcCsUST;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsUST.set(pAc-							User System test pending	
UCT	TsPendUsrConsist	TEST_PEND_USR_CONSIST	TESTS			18	bool	:1	bool bIcCsUCT;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsUCT.set(pAc-							User Consistency test pending	
UTBPL	TsPendUsrThresBpl	TEST_PEND_USR_THRESH_BPL	TESTS			17	bool	:1	bool bIcCsUTBPL;1;	name="bIcCsUTBPL" multiplexing-	field-ref-item field-	>bIcCsUTBPL.set(pAc-	(data.itemAvailable("b							User Threshold to BPL pending
UET	TsPendUsrEngy	TEST_PEND_USR_ENGY	TESTS			16	bool	:1	bool bIcCsUET;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsUET.set(pAc-	(data.itemAvailable("b							User Energy Test pending
UBPBIS	TsPendUsrBmPermis	TEST_PEND_USR_BP_BIS	TESTS			15	bool	:1	bool bIcCsUBPBIS;1;	name="bIcCsUBPBIS" multiplexing-	field-ref-item field-	>bIcCsUBPBIS.set(pA							User BPBIS (BLECS to Beam interlock) pending	
EST	TsPendExpSysTest	TEST_PEND_EXP_SYS_TST	TESTS			14	bool	:1	bool bIcCsEST;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsEST.set(pAc-							Expert System test pending	
ECT	TsPendExpConsist	TEST_PEND_EXP_CONSIST	TESTS			13	bool	:1	bool bIcCsECT;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsECT.set(pAc-							Expert Consistency test pending	
TPESTPH	-	TEST_PEND_EXP_STOP_HV	TESTS			12	bool	:1	bool bIcCsTPESTPH;1;	name="bIcCsTPESTPH"	field-ref-item field-	>bIcCsTPESTPH.set							Expert stop of the HV pending	
TPERSTFGA	-	TEST_PEND_EXP_RST_FPGA	TESTS			11	bool	:1	bool bIcCsPERSTFGA;1;	name="bIcCsPERSTFGA"	field-ref-item field-	>bIcCsPERSTFGA.							Expert reset of the tunnel FPGA pending	
EBPBIS	TsPendExpBnPermBis	TEST_PEND_EXP_BP_BIS	TESTS			10	bool	:1	bool bIcCsEBPBIS;1;	name="bIcCsEBPBIS" multiplexing-	field-ref-item field-	>bIcCsEBPBIS.set(pA							Expert BPBIS (BLECS to Beam interlock) pending	
EBPTC	TsPendExpBnPermc	TEST_PEND_EXP_BP_TC	TESTS			9	bool	:1	bool bIcCsEBPTC;1;	name="bIcCsEBPTC" multiplexing-	field-ref-item field-	>bIcCsEBPTC.set(pAc-							Expert BPTC (Beam Permit BLETC to BLECS) pending	
EHLVL	TsPendExpLowFreqModul	TEST_PEND_EXP_LF_MOD	TESTS			8	bool	:1	bool bIcCsEHLVL;1;	name="bIcCsEHLVL" multiplexing-	field-ref-item field-	>bIcCsEHLVL.set(pAc-							Expert HVL test pending	
EHVFC	TsPendExpCfcTest	TEST_PEND_EXP_CFC_TST	TESTS			7	bool	:1	bool bIcCsEHVFC;1;	name="bIcCsEHVFC"	field-ref-item field-	>bIcCsEHVFC.set(p							Expert HVCFC (100 pA test) pending	
EHRDAC	TsPendExpCfrDac	TEST_PEND_EXP_RST_DAC	TESTS			6	bool	:1	bool bIcCsEHRDAC;1;	name="bIcCsEHRDAC"	field-ref-item field-	>bIcCsEHRDAC.set							Expert HRDAC (Dac Reset) pending	
EVRGOH	TsPendExpRstGoh	TEST_PEND_EXP_RST_GOH	TESTS			5	bool	:1	bool bIcCsEVROGH;1;	name="bIcCsEVROGH"	field-ref-item field-	>bIcCsEVROGH.set							Expert VRGOH (Goh Reset) pending	
EMC	TsPendExpManCtrl	TEST_PEND_EXP_MAN_CTRL	TESTS			4	bool	:1	bool bIcCsEMC;1;	name="bIcCsEMC" multiplexing-	field-ref-item field-	>bIcCsEMC.set(pAc-							Expert Manual control Request pending	
TST	TsPendExpSysTest	TEST_PEND_TMR_SYS_TST	TESTS			3	bool	:1	bool bIcCsTST;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsTST.set(pAc-	(data.itemAvailable("b							Timer System Test pending
TSTRN	TsPendTimerSysTestNorm	TEST_TIMER_RQST_SYS_TST_LO	TESTS			2	bool	:1	bool bIcCsTSTRN;1;	name="bIcCsTSTRN" multiplexing-	field-ref-item field-	>bIcCsTSTRN.set(pAc-							Timer is requesting a test of the system with normal priority	
TSTRP	TsPendTimerSysTestPro	TEST_TIMER_RQST_SYS_TST_HI	TESTS			1	bool	:1	bool bIcCsTSTRP;1;	name="bIcCsTSTRP" multiplexing-	field-ref-item field-	>bIcCsTSTRP.set(pAc-							Timer is requesting a test of the system with high priority (BeamPermit away next Dump)	
LCBIBU	LastCombibfCibus	OVERVIEW			0	bool	:1	bool bIcCsLCBIBU;1;	name="bIcCsLCBIBU" multiplexing-	field-ref-item field-	>bIcCsLCBIBU.set(pA	(data.itemAvailable("b							BLECS (Combiner) is the last one before the CIBU interface / controls the HV	
NCCTR	TimeBfrNxtConsistTstCritical	TIME_BFR_NXT_CONSIST_TST_CRT	TESTS	700004	700008	unsigned	:32	unsigned	bIcCsNCCTR;32;	name="bIcCsNCCTR" multiplexing-	field-ref-item field-	>bIcCsNCCTR.set(pA	(data.itemAvailable("b							Next critical consistency test request
NCCSTR	TimeBfrNxtSysTstCritical	TIME_BFR_NXT_SYS_TST_CRT	TESTS	700008	70000C	unsigned	:32	unsigned	bIcCsNCCSTR;32;	name="bIcCsNCCSTR" multiplexing-	field-ref-item field-	>bIcCsNCCSTR.set(pA	(data.itemAvailable("b							Time remained before critical system test [s]
AST	TsActiveSysTest	TEST_ACTIVE_SYS_TST	TESTS	70000C	700010	31	bool	:1	bool bIcCsAST;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsAST.set(pAc-							Active test overview	
ACT	TsActiveConsist	TEST_ACTIVE_CONSIST	TESTS			30	bool	:1	bool bIcCsACT;1;	multiplexing-criterion="NONE"	field-ref-item field-	>bIcCsACT.set(pAc-							Active test : CONSISTENCY (threshold table control)	
ASTPHV	-	TEST_ACTIVE_STOP_HV	TESTS			29	bool	:1	bool bIcCsASTPHV;1;	name="bIcCsASTPHV"	field-ref-item field-	>bIcCsASTPHV.set(p							Active test : STOP of the HV	
ARSTFGA	-	TEST_ACTIVE_RST_FPGA	TESTS			28	bool	:1	bool bIcCsARSTFGA;1;	name="bIcCsARSTFGA"	field-ref-item field-	>bIcCsARSTFGA.set							Active test : Reset of the tunnel FPGA	
ABPBIS	TsActiveBmPermis	TEST_ACTIVE_BP_BIS	TESTS			27	bool	:1	bool bIcCsABPBIS;1;	name="bIcCsABPBIS" multiplexing-	field-ref-item field-	>bIcCsABPBIS.set(pA							Active test : BPBIS (Beam Permit lines from the last combiner to the LBIS)	
ABPTC	TsActiveBmPermc	TEST_ACTIVE_BP_TC	TESTS			26	bool	:1	bool bIcCsABPTC;1;	name="bIcCsABPTC" multiplexing-	field-ref-item field-	>bIcCsABPTC.set(pAc-							Active test : BPTC (Threshold comparator Beam Permit to last Combiner before CIBUS)	
AHVLF	TsActiveLowFreqModul	TEST_ACTIVE_LF_MOD	TESTS			25	bool	:1	bool bIcCsAHVLF;1;	name="bIcCsAHVLF" multiplexing-	field-ref-item field-	>bIcCsAHVLF.set(pAc-							Active test : HVL (Modulation of the high voltage and analysis)	
AHVFC	TsActiveCfcTest	TEST_ACTIVE_CFC_TST	TESTS			24	bool	:1	bool bIcCsAHVFC;1;	name="bIcCsAHVFC"	field-ref-item field-	>bIcCsAHVFC.set(p							Active test : CFC (Activation Test CFC "100pA" mode of the tunnel card)	
AHRDAC	TsActiveRsxDac	TEST_ACTIVE_RST_DAC	TESTS			23	bool	:1	bool bIcCsAHRDAC;1;	name="bIcCsAHRDAC"										

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BPLUTA	BmPermTcActStUmsk		Beam PERMIT		5	bool	:1	bool blecsBPLUTA;1;	name="blecsBPLUTA" multiplexing	field-ref-item field-	>blecsBPLUTA.set(pA	(data.itemAvailable)"b								Beam permit line unmaskable test activation (send to BLETC cards)		
BPMFTA	BmPermTcActStMsk		Beam PERMIT		4	bool	:1	bool blecsBPMFTA;1;	name="blecsBPMFTA"	field-ref-item field-	>blecsBPMFTA.set(p	(data.itemAvailable)"b								Beam permit line maskable test activation (send to BLETC cards)		
BPLTCN	BmPermTcActCrdNbr		Beam PERMIT		0..3	unsigned	:4	unsigned blecsBPLTCN;4;	name="blecsBPLTCN"	field-ref-item field-	>blecsBPLTCN.set(p	(data.itemAvailable)"b								Beam permit line test TC card number (send to BLETC cards)		
BEFCFA	BmEngyFrmCntA		Beam ENERGY	700034	700038	16..31	unsigned	:16	unsigned blecsBEFCFA;16;	name="blecsBEFCFA" multiplexing	field-ref-item field-	>blecsBEFCFA.set(pAc	(data.itemAvailable)"b							Beam Energy 2		
BEFCB	BmEngyFrmCntB		Beam ENERGY		0..15	unsigned	:16	unsigned blecsBEFCB;16;	name="blecsBEFCB" multiplexing	field-ref-item field-	>blecsBEFCB.set(pAc	(data.itemAvailable)"b							Beam energy reception Frame counter channel B			
BECRCA	BmEngyCrcCntA	BEAM_ENGY_CRC_CNT_A	Beam ENERGY	700038	70003C	16..31	unsigned	:16	unsigned blecsBECRCA;16;	name="blecsBECRCA"	field-ref-item field-	>blecsBECRCA.set(p								Number of CRC errors at the reception of the beam energy (channel A) from the CISV card (AB/CO)		
BECRCB	BmEngyCrcCntB	BEAM_ENGY_CRC_CNT_B	Beam ENERGY		0..15	unsigned	:16	unsigned blecsBECRCB;16;	name="blecsBECRCB"	field-ref-item field-	>blecsBECRCB.set(p								Number of CRC errors at the reception of the beam energy (channel B) from the CISV card (AB/CO)			
BELFA	BmEngyLstFrmCntA	BEAM_ENGY_LOSTF_CNT_A	Beam ENERGY	70003C	700040	16..31	unsigned	:16	unsigned blecsBELFA;16;	name="blecsBELFA" multiplexing	field-ref-item field-	>blecsBELFA.set(pAc								Beam Energy 4		
BELFB	BmEngyLstFrmCntB	BEAM_ENGY_LOSTF_CNT_B	Beam ENERGY		0..15	unsigned	:16	unsigned blecsBELFB;16;	name="blecsBELFB" multiplexing	field-ref-item field-	>blecsBELFB.set(pAc								Number of lost frames (packets) at the reception of the beam energy (channel A) from the CISV card (AB/CO)			
BETOV	BmEngyTimeOut	BEAM_ENGY_TIME_OUT	Beam ENERGY	700040	700044	28..31	unsigned	:4	unsigned blecsBETOV;4;	name="blecsBETOV" multiplexing	field-ref-item field-	>blecsBETOV.set(pAc								Number of TIMEOUTS at the reception of the beam energy value from the CISV card (AB/CO)		
V1H	HighV1VHigh	HV_1_VOLT_HIGH			-		0..27	unsigned	:28	unsigned blecsSPARE118;28;									SPARE			
V1L	HighV1VLow	HV_1_VOLT_LOW			700044	700048	31	bool	:1	bool blecsV1H;1;	multiplexing-criterion="NONE"	field-ref-item field-	>blecsV1H.set(pAcq-							High Voltage 1 survey		
I1H	HighV1IHigh	HV_1_CURR_HIGH					30	bool	:1	bool blecsV1L;1;	multiplexing-criterion="NONE"	field-ref-item field-	>blecsV1L.set(pAcq-							High Voltage power supply 1 / voltage lower		
I1L	HighV1ILow	HV_1_CURR_LOW					29	bool	:1	bool blecsI1H;1;	multiplexing-criterion="NONE"	field-ref-item field-	>blecsI1H.set(pAcq-							High Voltage power supply 1 / current higher		
							28	bool	:1	bool blecsI1L;1;	multiplexing-criterion="NONE"	field-ref-item field-	>blecsI1L.set(pAcq-							High Voltage power supply 1 / current lower		
							24..27	unsigned	:4	unsigned blecsSPARE123;4;									SPARE			
							0..23	unsigned	:24	unsigned blecsHV1V;24;	name="blecsHV1V" multiplexing	field-ref-item field-	>blecsHV1V.set(pAcq-							High Voltage 2 survey		
V2H	HighV2VHigh	HV_2_VOLT_HIGH			Survey	700048	70004C	31	bool	:1	bool blecsV2H;1;	multiplexing-criterion="NONE"	field-ref-item field-	>blecsV2H.set(pAcq-							High Voltage power supply 2 / voltage higher	
V2L	HighV2VLow	HV_2_VOLT_LOW					30	bool	:1	bool blecsV2L;1;	multiplexing-criterion="NONE"	field-ref-item field-	>blecsV2L.set(pAcq-							High Voltage power supply 2 / voltage lower		
I2H	HighV2IHigh	HV_2_CURR_HIGH					29	bool	:1	bool blecsI2H;1;	multiplexing-criterion="NONE"	field-ref-item field-	>blecsI2H.set(pAcq-							High Voltage power supply 2 / current higher		
I2L	HighV2ILow	HV_2_CURR_LOW					28	bool	:1	bool blecsI2L;1;	multiplexing-criterion="NONE"	field-ref-item field-	>blecsI2L.set(pAcq-							High Voltage power supply 2 / current lower		
							24..27	unsigned	:4	unsigned blecsSPARE129;4;									SPARE			
							0..23	unsigned	:24	unsigned blecsHV2V;24;	name="blecsHV2V" multiplexing	field-ref-item field-	>blecsHV2V.set(pAcq-							High Voltage power supply 2 / voltage value in [V] from 24 bits [HV = 3000/2**24*code]		
HV1V	HighV1VValue	HV_1_VOLT_VALUE					70004C	700050	16..31	unsigned	:16	unsigned blecsHV1MAX;16;	name="blecsHV1MAX"	field-ref-item field-	>blecsHV1MAX.set(p	(data.itemAvailable)"b					High Voltage 1 MinMax	
HV1MIN	HighV1Min	HV_1_VMIN					0..15	unsigned	:16	unsigned blecsHV1MIN;16;	name="blecsHV1MIN" multiplexing	field-ref-item field-	>blecsHV1MIN.set(pA	(data.itemAvailable)"b						Min Value of HV1 since last read of the CPU (16bits) [hvmax = 3000/2**16*code] in V		
HV2MAX	HighV2VMax	HV_2_VMAX					700050	700054	16..31	unsigned	:16	unsigned blecsHV2MAX;16;	name="blecsHV2MAX"	field-ref-item field-	>blecsHV2MAX.set(p	(data.itemAvailable)"b					High Voltage 2 MinMax	
HV2MIN	HighV2Min	HV_2_VMIN						0..15	unsigned	:16	unsigned blecsHV2MIN;16;	name="blecsHV2MIN" multiplexing	field-ref-item field-	>blecsHV2MIN.set(pA	(data.itemAvailable)"b					Min Value of HV2 since last read of the CPU (16bits) [hvmax = 3000/2**16*code] in V		
HV1I	HighV1IValue	HV_1_CURR_VALUE					700054	700058	16..31	unsigned	:16	unsigned blecsHV1I;16;	name="blecsHV1I" multiplexing	field-ref-item field-	>blecsHV1I.set(pAcq-						High Voltage Currents	
HV2I	HighV2IValue	HV_2_CURR_VALUE						0..15	unsigned	:16	unsigned blecsHV2I;16;	name="blecsHV2I" multiplexing	field-ref-item field-	>blecsHV2I.set(pAcq-						High Voltage power supply 2 / current value (16 bits) [I = 20/2**16*code] in mA		
HV1IMAX	HighV1IMax	HV_1_IMAX						700058	70005C	16..31	unsigned	:16	unsigned blecsHV1IMAX;16;	name="blecsHV1IMAX"	field-ref-item field-	>blecsHV1IMAX.set(p	(data.itemAvailable)"b				High Voltage 1 Current MinMax	
HV1MIN	HighV1Min	HV_1_VMIN							0..15	unsigned	:16	unsigned blecsHV1MIN;16;	name="blecsHV1MIN"	field-ref-item field-	>blecsHV1MIN.set(pA	(data.itemAvailable)"b				Min Value of the HV1 current since last read of the CPU (16bits) [hvmax = 3000/2**16*code] in mA		
HV2IMAX	HighV2IMax	HV_2_IMAX						70005C	700060	16..31	unsigned	:16	unsigned blecsHV2IMAX;16;	name="blecsHV2IMAX"	field-ref-item field-	>blecsHV2IMAX.set(p	(data.itemAvailable)"b				High Voltage 2 Current MinMax	
HV2MIN	HighV2Min	HV_2_VMIN							0..15	unsigned	:16	unsigned blecsHV2MIN;16;	name="blecsHV2MIN"	field-ref-item field-	>blecsHV2MIN.set(pA	(data.itemAvailable)"b				Min Value of the HV2 current since last read of the CPU (16bits) [I = 20/2**16*code] in mA		
VME3V3	Vme3Volt3Value						700060	700064	29..31	unsigned	:3	unsigned blecsSPARE14;3;								VME Voltages 1		
									16..28	unsigned	:13	unsigned blecsVME3V3;13;	name="blecsVME3V3" multiplexing	field-ref-item field-	>blecsVME3V3.set(pA	(data.itemAvailable)"b					VME 3V (13 bits) with 10V full scale	
									13..15	unsigned	:3	unsigned blecsSPARE14;3;								SPARE		
VME3V3DEL	Vme3Volt3Delta							700064	700068	29..31	unsigned	:13	unsigned blecsVME3V3DEL;13;	name="blecsVME3V3DEL"	field-ref-item field-	>blecsVME3V3DELS	(data.itemAvailable)"b					Delta value of the VME3V3 => Vmax-Vmin [V=10/2**13*code] in V
VME5V	Vme5VoltValue								16..28	unsigned	:13	unsigned blecsVME5V;13;	name="blecsVME5V" multiplexing	field-ref-item field-	>blecsVME5V.set(pAc	(data.itemAvailable)"b					VME 5V (13 bits) with 10V full scale	
									13..15	unsigned	:3	unsigned blecsSPARE14;3;								SPARE		
VME5VDEL	Vme5VoltDelta								0..12	unsigned	:13	unsigned blecsVME5VDEL;13;	name="blecsVME5VDEL"	field-ref-item field-	>blecsVME5VDEL.set	(data.itemAvailable)"b					Delta value of the VME5V => Vmax-Vmin [V=10/2**13*code] in V	
								700068	70006C	29..31	unsigned	:3	unsigned blecsSPARE14;3;							Analog Voltages 1		
AV5V	Analog5VoltValue								16..28	unsigned	:13	unsigned blecsAV5V;13;	name="blecsAV5V" multiplexing	field-ref-item field-	>blecsAV5V.set(pAcq-						Analog 5V (13 bits) with 10V full scale	
									13..15	unsigned	:3	unsigned blecsSPARE15;3;								SPARE		
AV5VDEL	Analog5VoltDelta								0..12	unsigned	:13	unsigned blecsAV5VDEL;13;	name="blecsAV5VDEL"	field-ref-item field-	>blecsAV5VDEL.set(p	(data.itemAvailable)"b						Delta value of the AV5V => Vmax-Vmin [V=10/2**13*code] in V
AVP15V	AnalogP15voltValue							70006C	700070	29..31	unsigned	:3	unsigned blecsSPARE15;3;							Analog P15V reference (13 bits) [V = 10/2**13*code*3] in V		
									16..28	unsigned	:13	unsigned blecsAVP15V;13;	name="blecsAVP15V"									



ETRBPL	ExtTstResThresBpl		-		2	bool	:1	bool blecsETRTBPL_1;	name="blecsETRTBPL"	field-ref-item field-	>blecsETRTBPL.set(p (data.itemAvailable("b								External Test result: Threshold to BPL given by the CPU 1=passed, 0=failed	
ETRCTYR	ExtTstResRdyConsist		-		1	bool	:1	bool blecsETRCTYR_1;	name="blecsETRCTYR"	field-ref-item field-	>blecsETRCTYR.set( (data.itemAvailable("b								External Test result ready: Consistency	
ETRCTY	ExtTstResConsist		-		0	bool	:1	bool blecsETRCTY_1;	name="blecsETRCTY"	field-ref-item field-	>blecsETRCTY.set(p (data.itemAvailable("b								External Test result: Consistency given by the CPU 1=passed, 0=failed	
ETRTPBLETC	-	702174	702178	16..31	unsigned	:16	unsigned	blecsETRTPBLETC_16;	name="blecsETRTPBLETC"	field-ref-item field-	>blecsETRTPBLETC.set(p (data.itemAvailable("b								ExternalTestResultPerBLETC	
ETRCPBLET	-		-		0..15	unsigned	:16	unsigned	blecsETRCPBLET_16;	name="blecsETRCPBLET"	field-ref-item field-	>blecsETRCPBLET.set(p (data.itemAvailable("b								Treshold to BPL result per BLETC, MSBit=BLETC_1, LSBit=BLETC_16, 1=passed, 0=failed
			702178	70217C	5..31	unsigned	:27	unsigned	blecsSPARE327_27;									Consistency result per BLETC, MSBit=BLETC_1, LSBit=BLETC_16, 1=passed, 0=failed		
																		ExternalControl_BPlines		
BISFMB	-				4	bool	:1	bool blecsBISFMB_1;	name="blecsBISFMB"	multiplexing-field-ref-item field-	>blecsBISFMB.set(pA (data.itemAvailable("b								BIS force beam permit maskable B	
BISFMA	-				3	bool	:1	bool blecsBISFMA_1;	name="blecsBISFMA"	multiplexing-field-ref-item field-	>blecsBISFMA.set(pA (data.itemAvailable("b								BIS force beam permit maskable A	
BISFUB	-				2	bool	:1	bool blecsBISFUB_1;	name="blecsBISFUB"	multiplexing-field-ref-item field-	>blecsBISFUB.set(pA (data.itemAvailable("b								BIS force beam permit Unmaskable B	
BISFUA	-				1	bool	:1	bool blecsBISFUA_1;	name="blecsBISFUA"	multiplexing-field-ref-item field-	>blecsBISFUA.set(pA (data.itemAvailable("b								BIS force beam permit Unmaskable A	
					0	bool	:1	bool blecsSPARE332_1;										SPARE		
					70217C	702200	unsigned	[33]	unsigned blecsSPARE333[33];										SPARE	