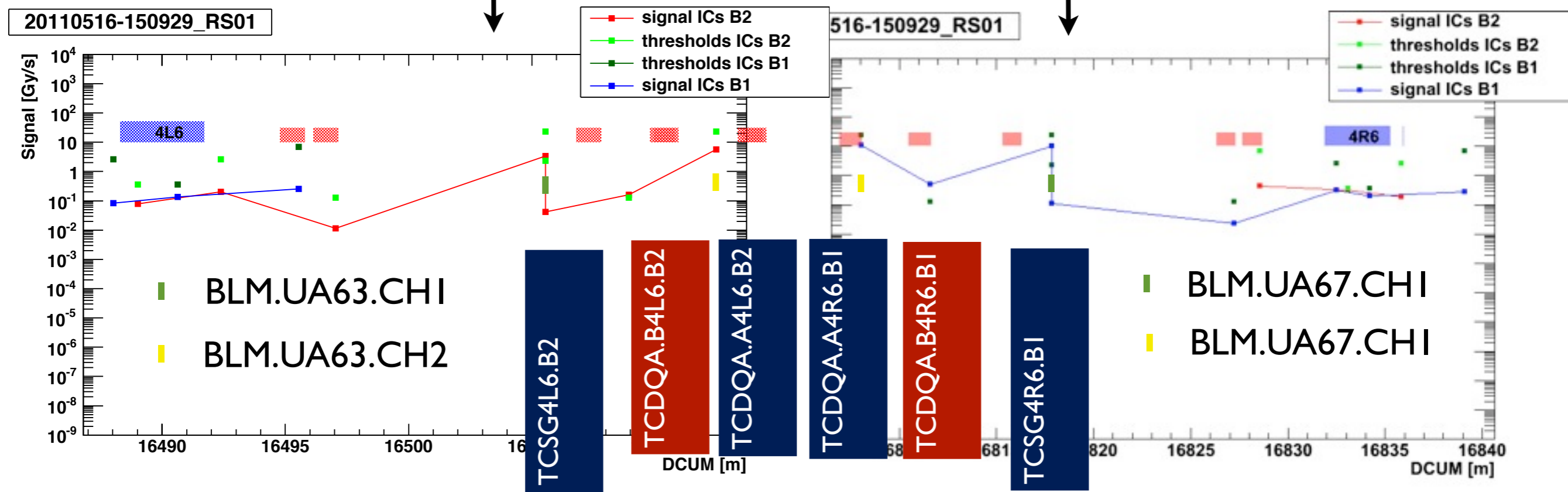
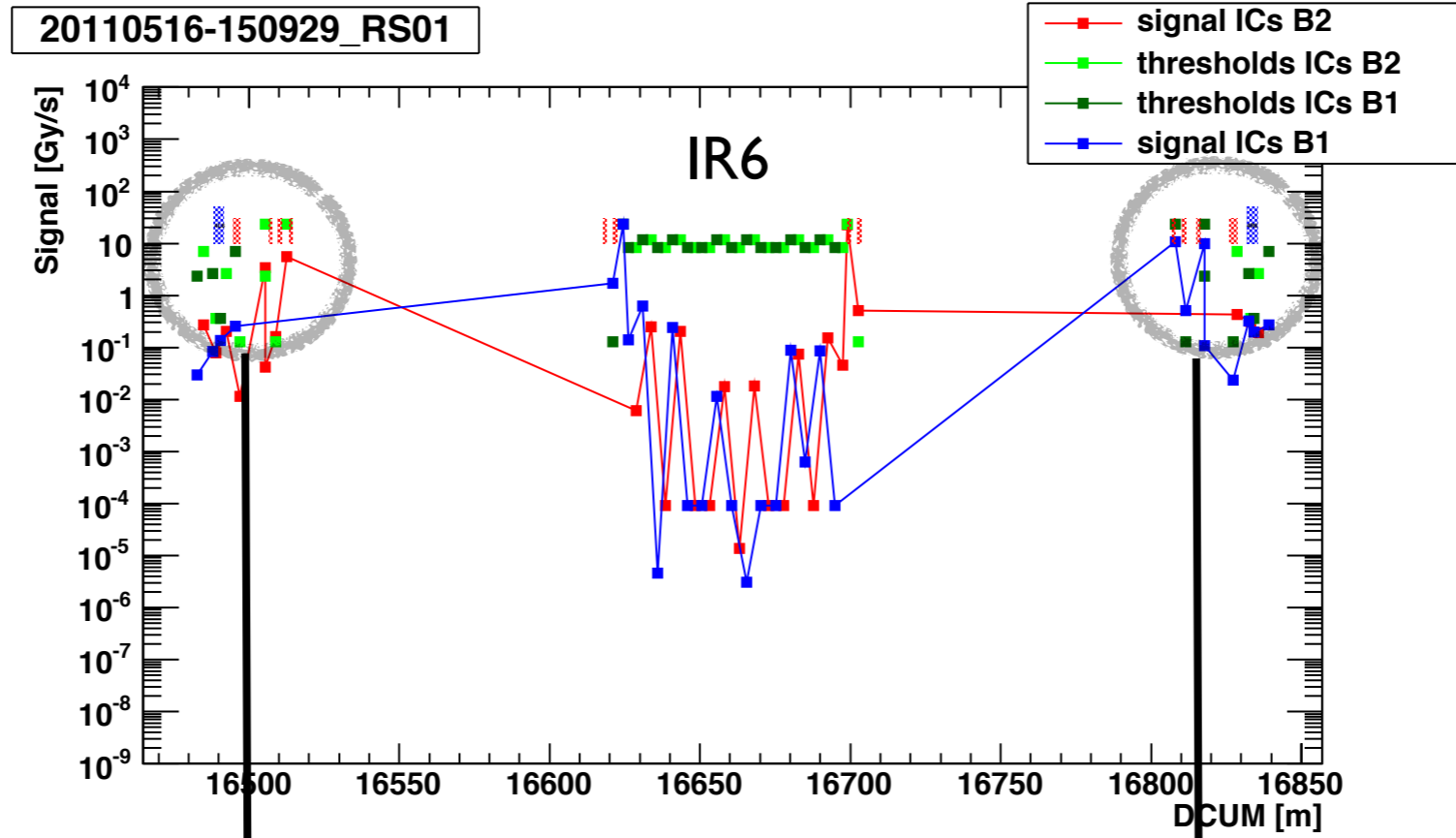


Update on Direct Dump BLMs

B. Dehning, E. Effinger, E. Holzer, E. Nebot
TWGM 27-05-2011

Location of the Direct Dump monitors



Observed Signals

- Analysis of highest (meaningful) signals between 13/05 00:00:00 and 26/06 00:00:00 (UTC). Signals (40us) in monitors protecting TCSGs or TCDQAs higher than 5 Gy/s. (62 Signals)

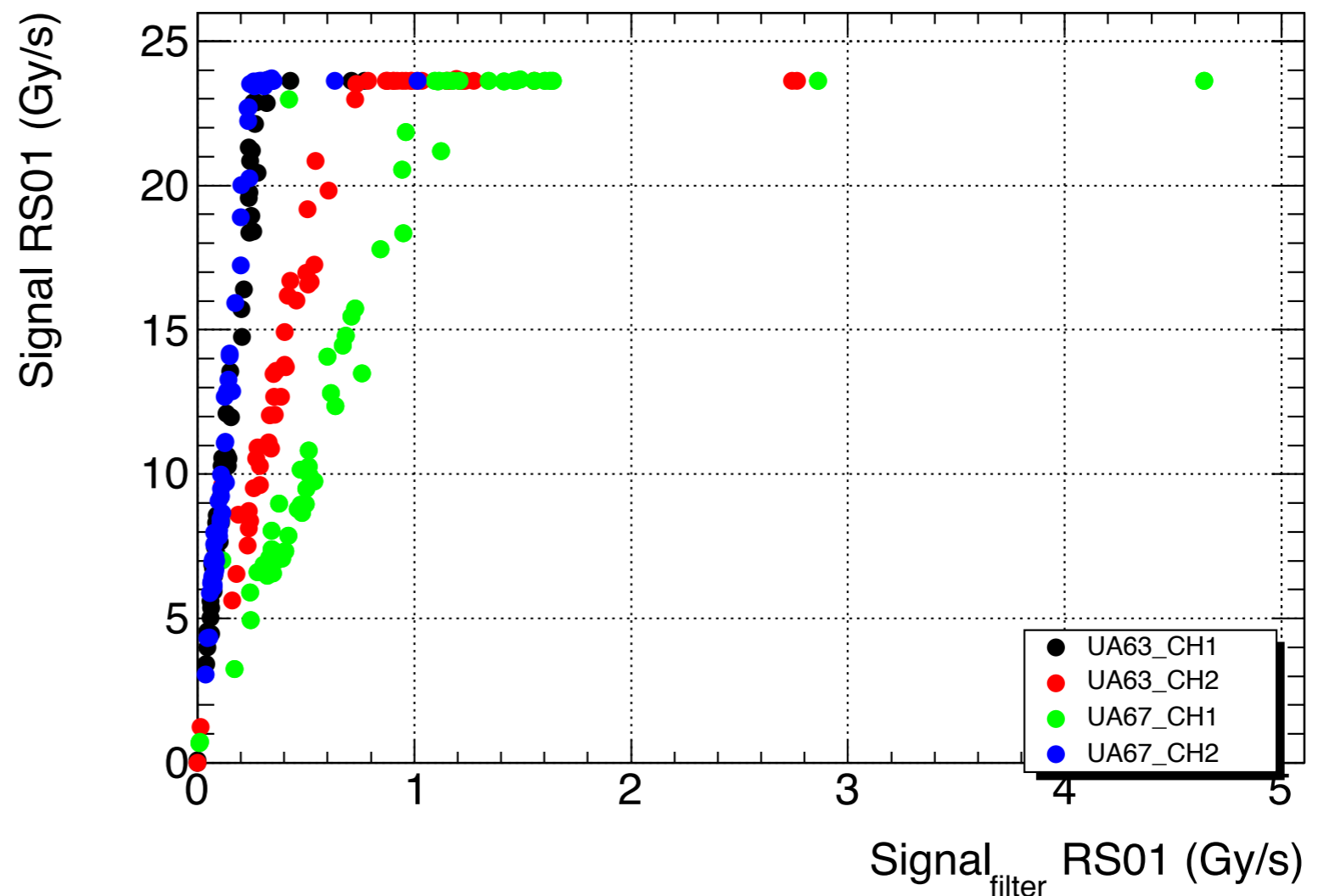
BLM.UA63.CHI = TCSG.B2

BLM.UA67.CHI = TCDQA.B1

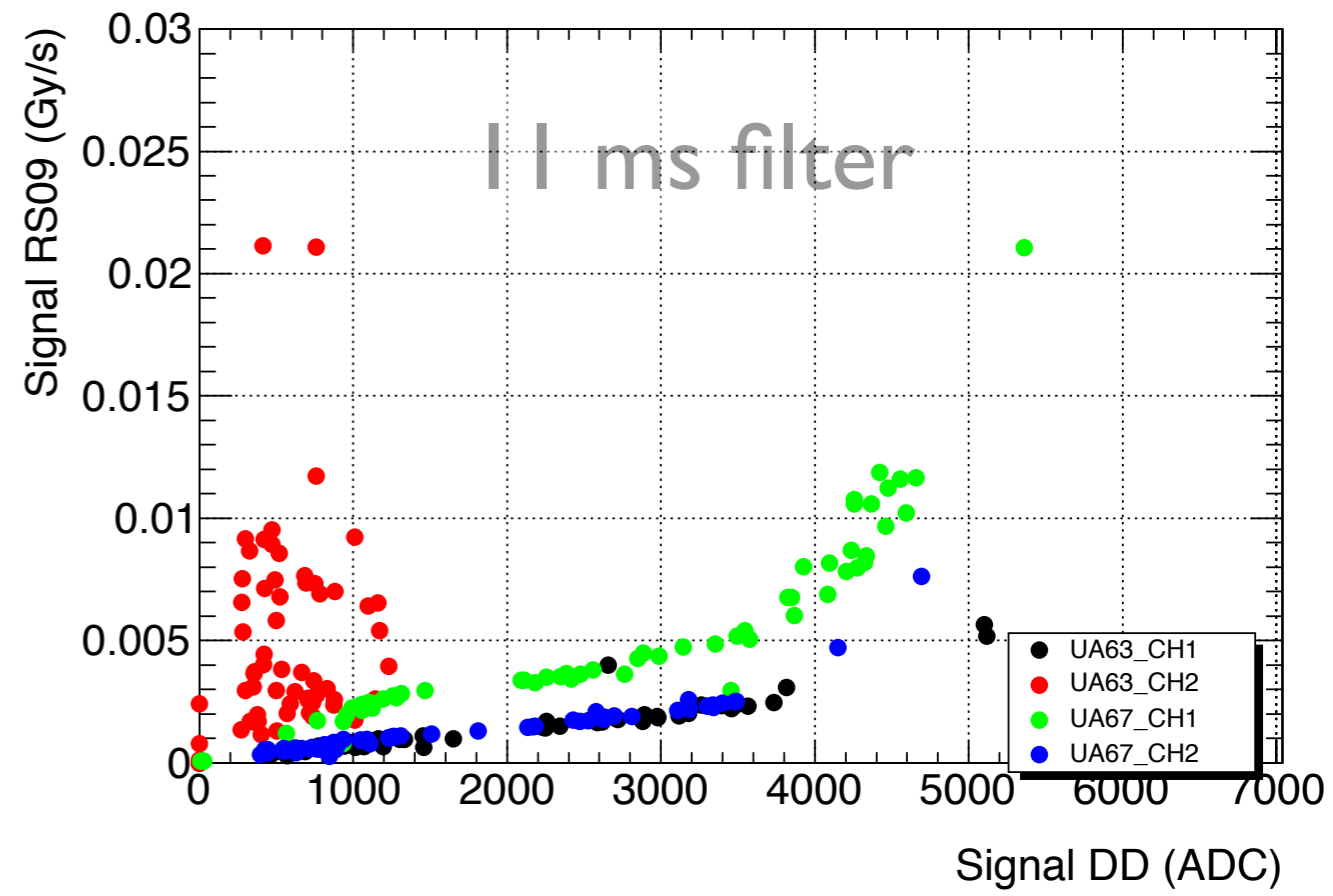
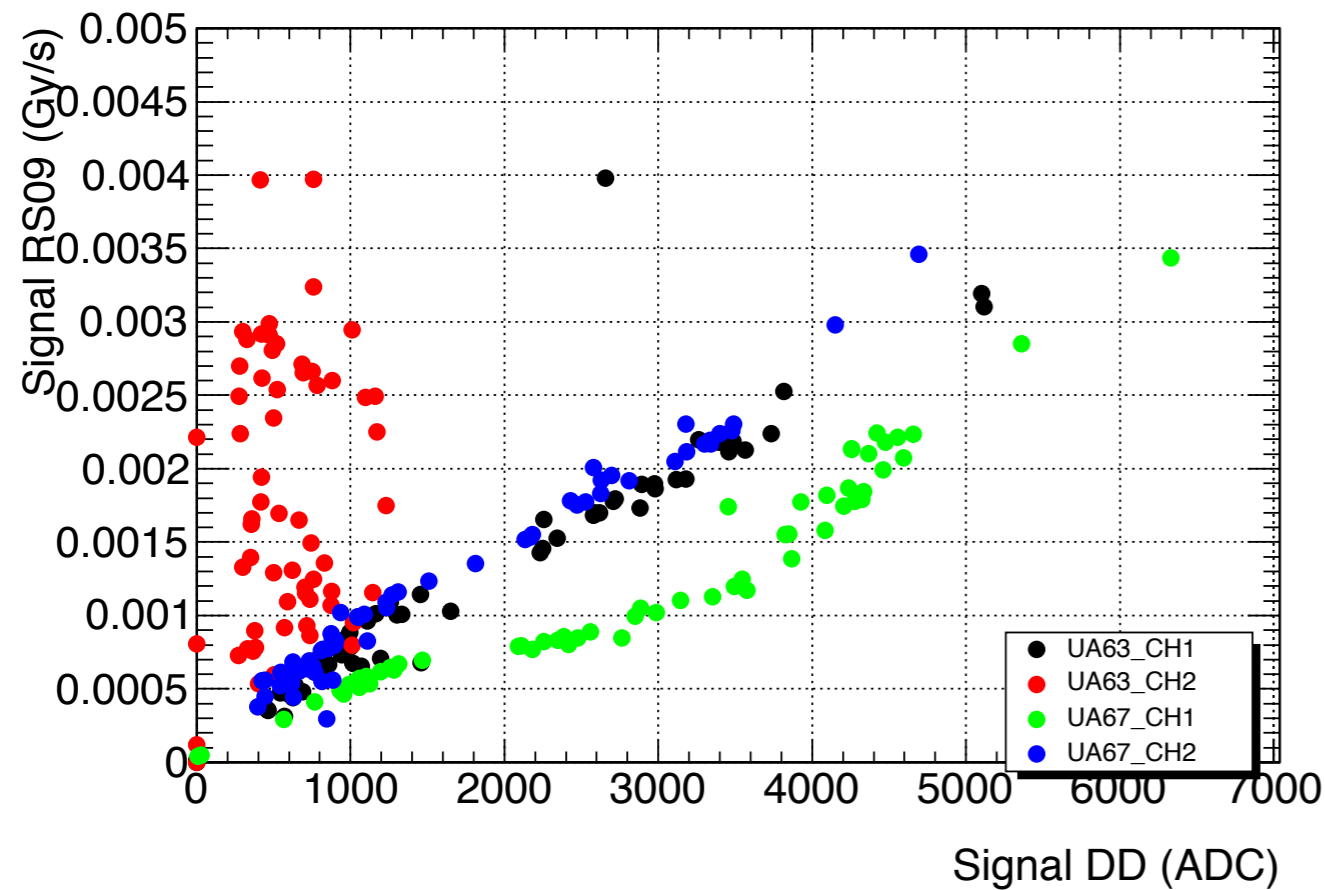
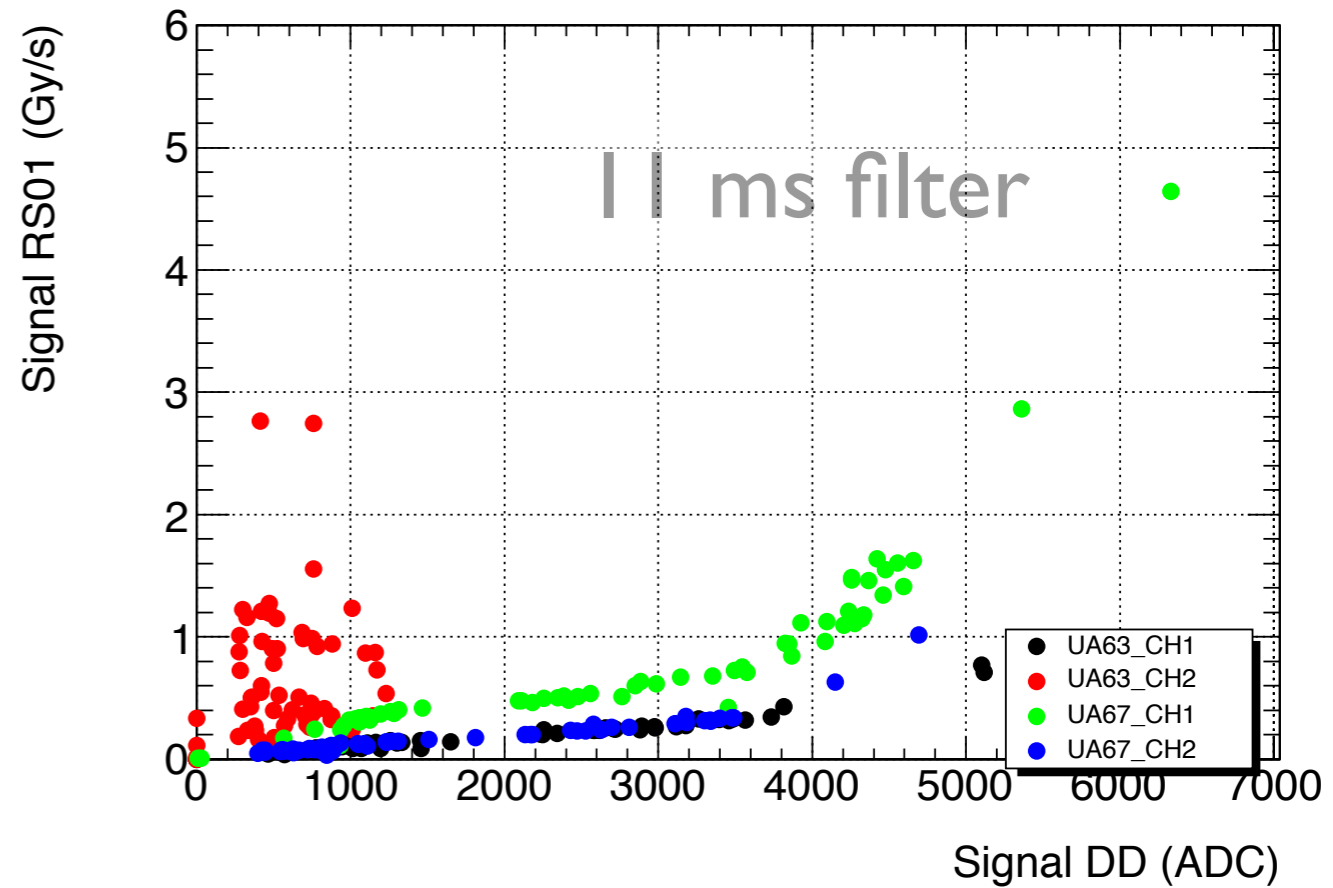
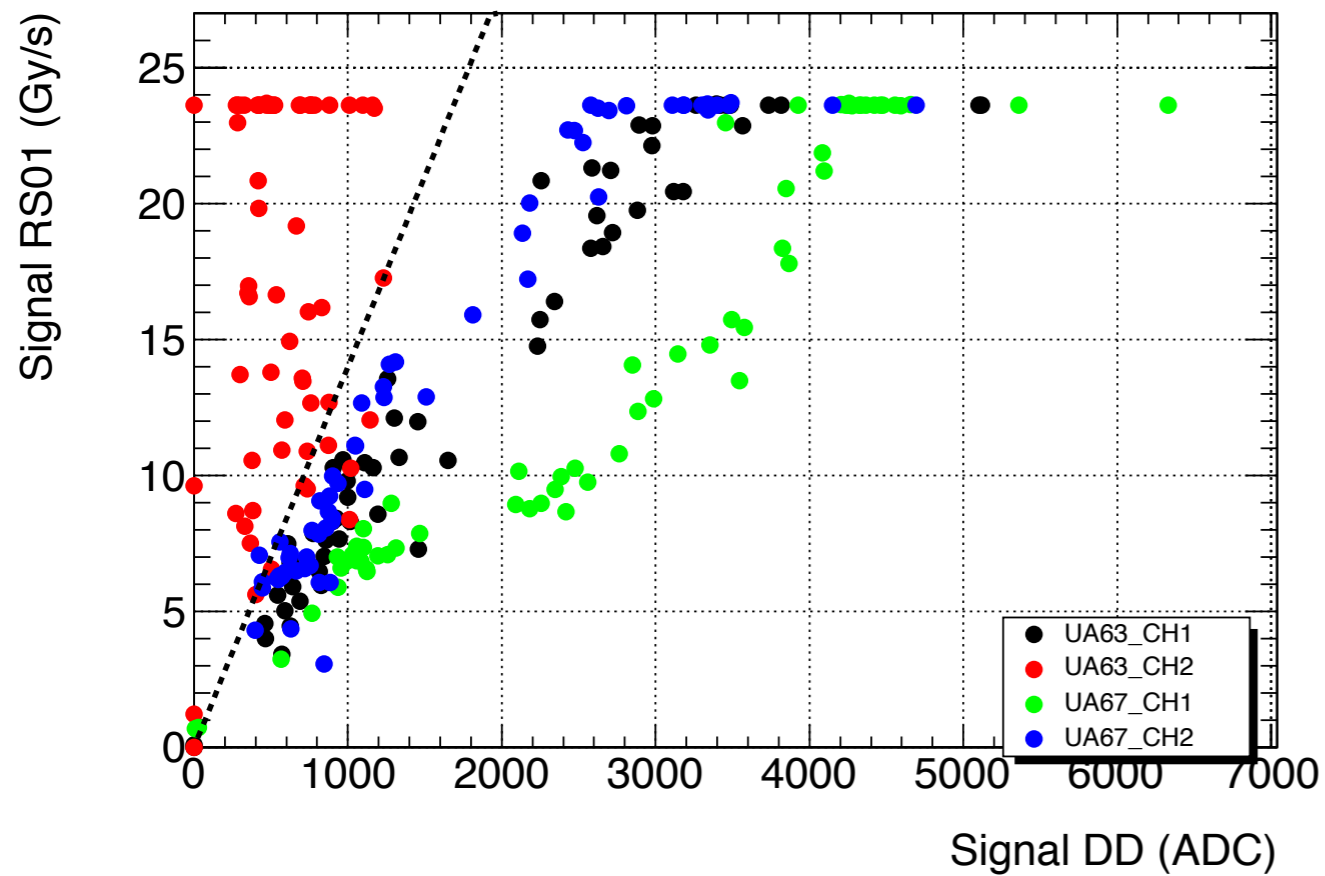
BLM.UA63.CH2 = TCDQA.B2

BLM.UA67.CH2 = TCSG.B2

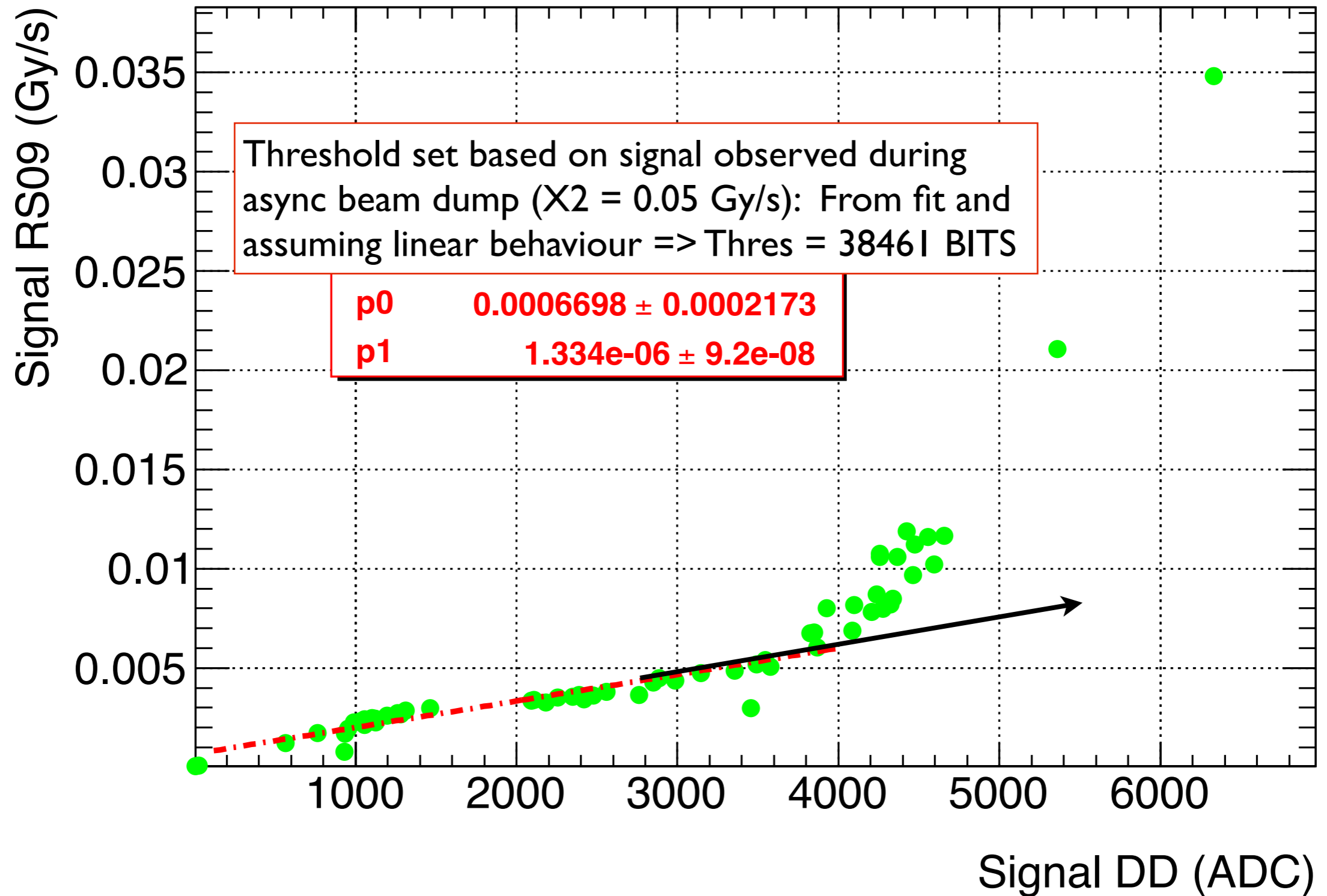
Comparison of signals in IC and IC with filter with standard read-out (signal integrated in 40us)



Observed Signals

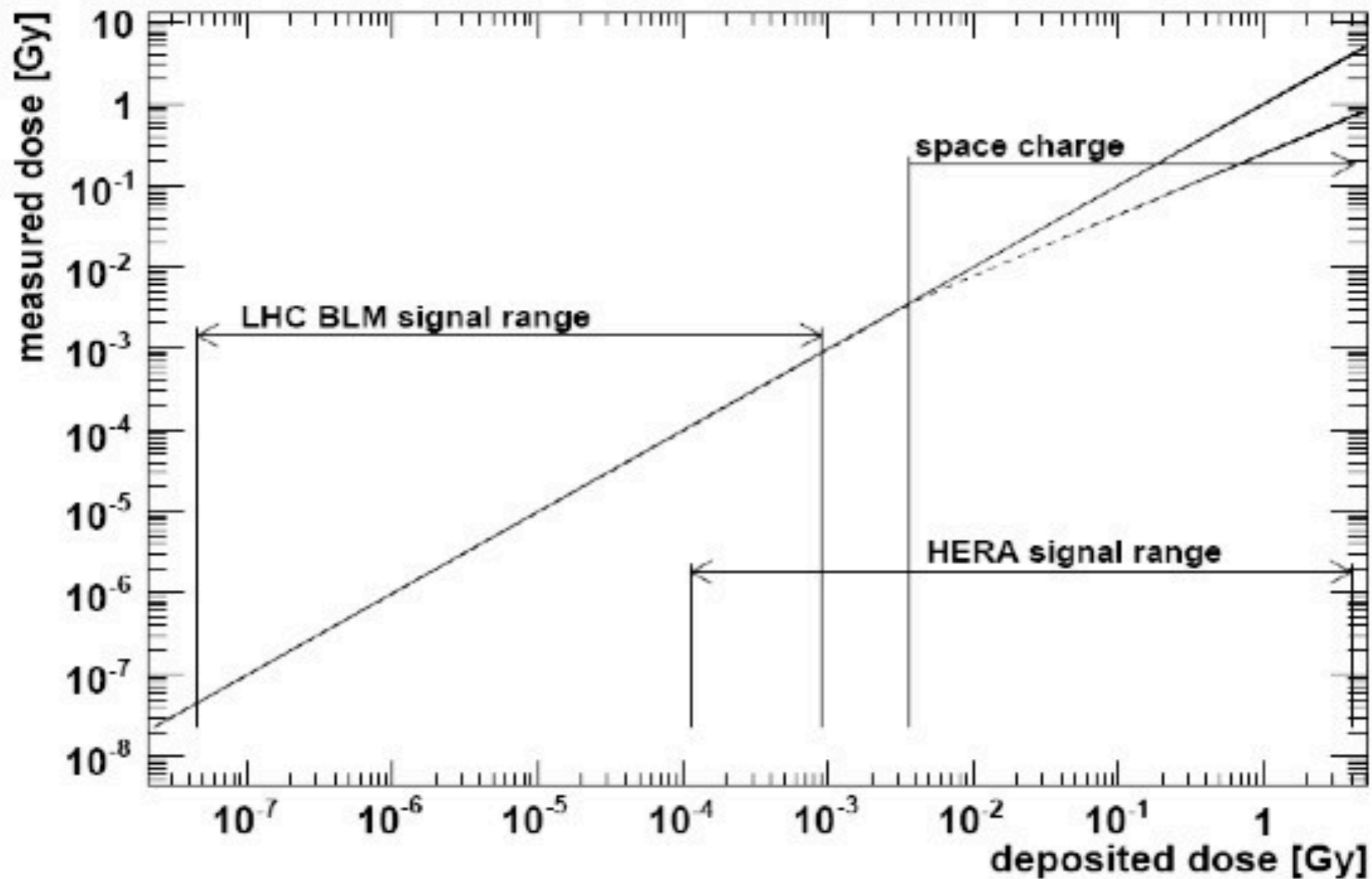


Thresholds set to 44590 BITS



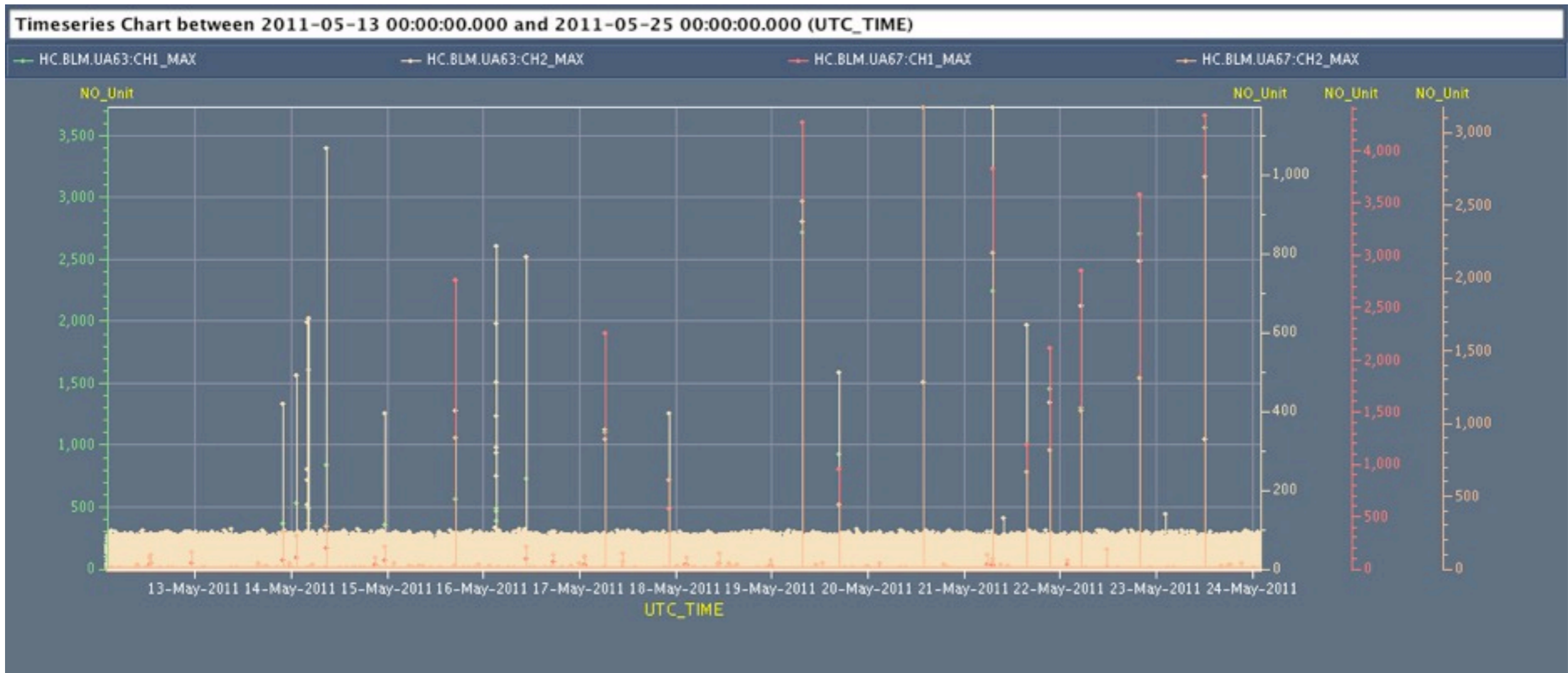
Back Up

Observed Signals

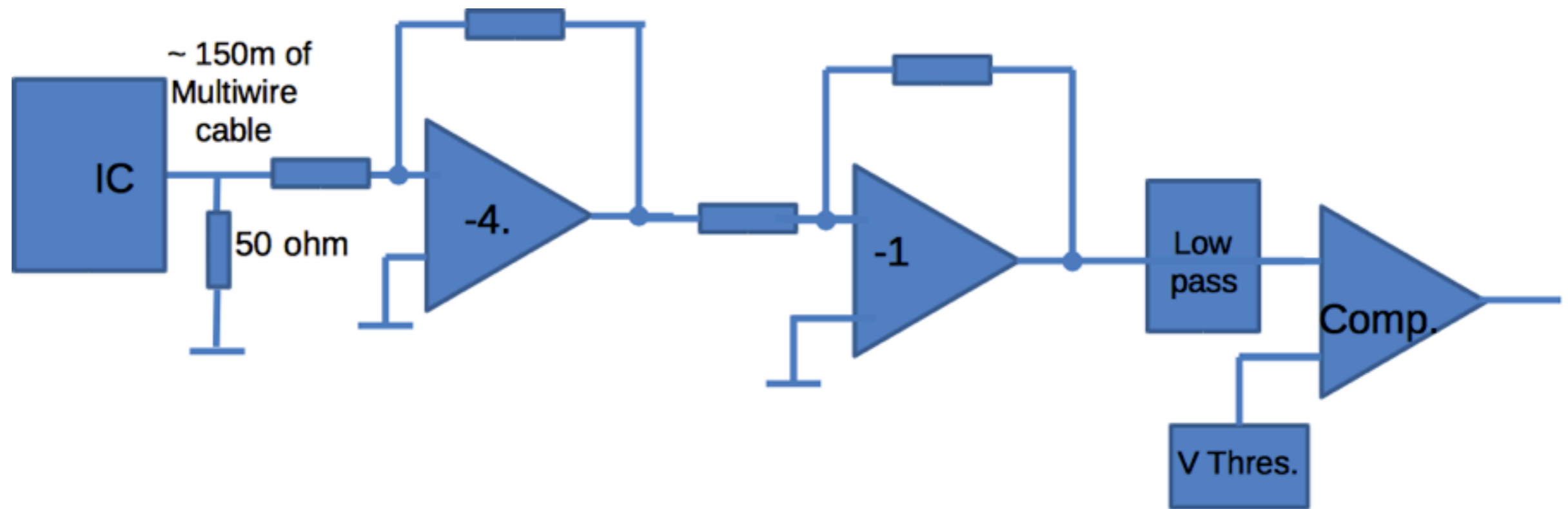


Observed Signals

- Clear spikes observed in BLMDD during beam dumps



Direct Dump Circuit



- Dynamic range 65536 Bits => 10V => 50mA
- Currently not connected to LBDS
- Threshold 44590 Bits => 6.8V => 34mA => $0.0252\text{Gy}/40\mu\text{s} = 630.17\text{Gy/s}$. This threshold was set based on signals observed in BLMEI.04L6.B2I10_TCDQA.B4L6 (11 ms filter) during asynchronous dump