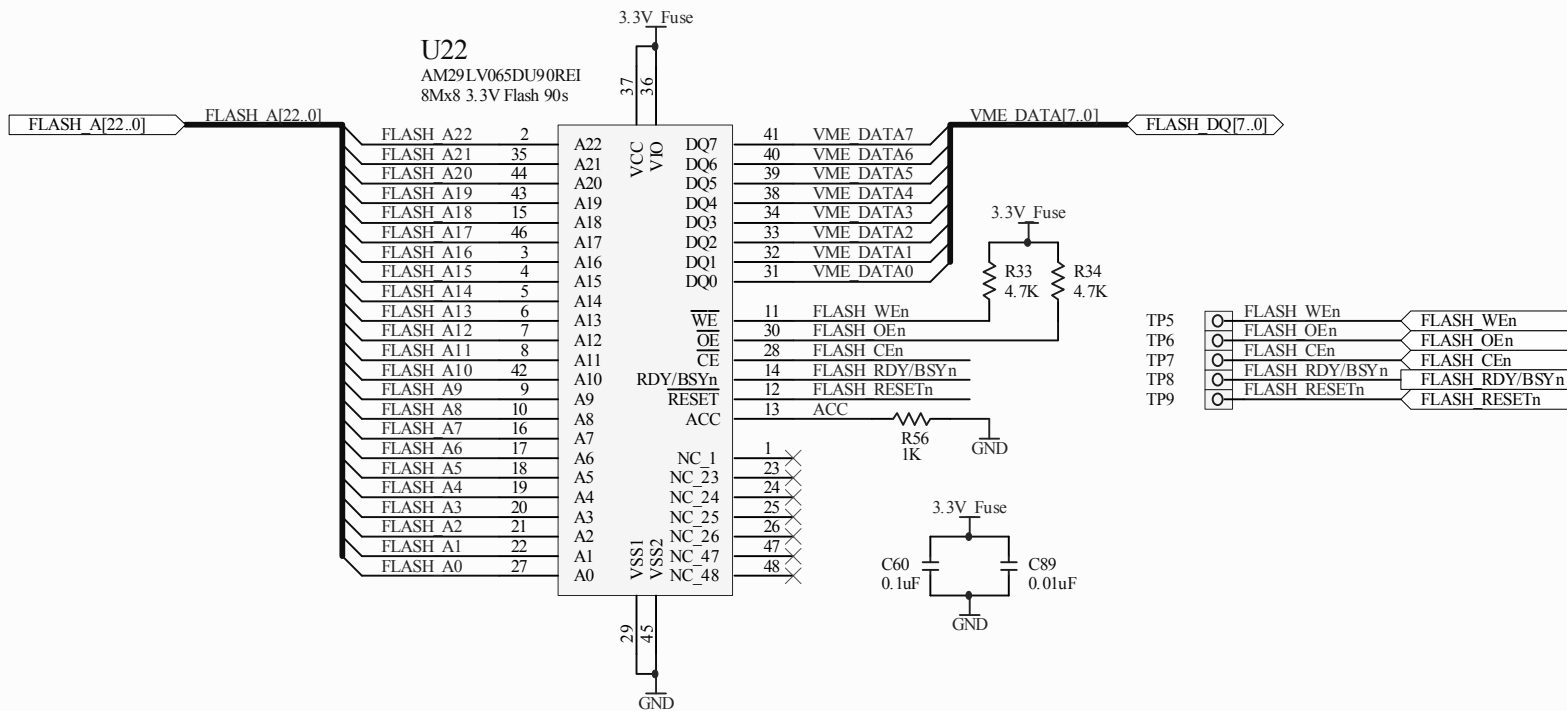
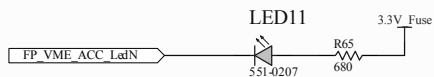
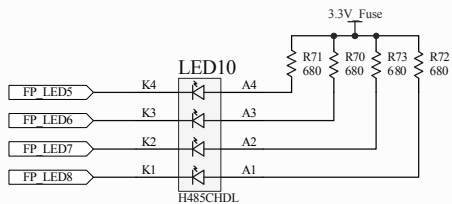
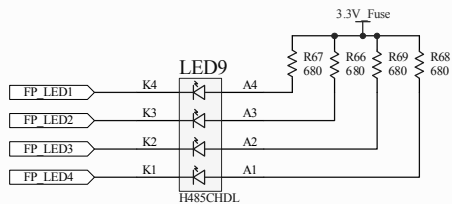
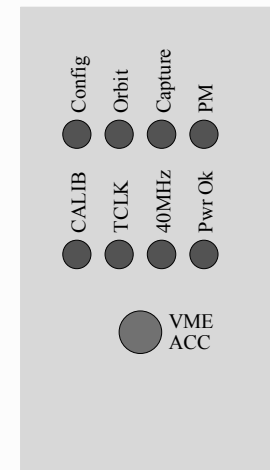
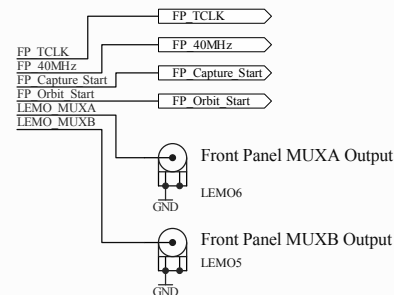
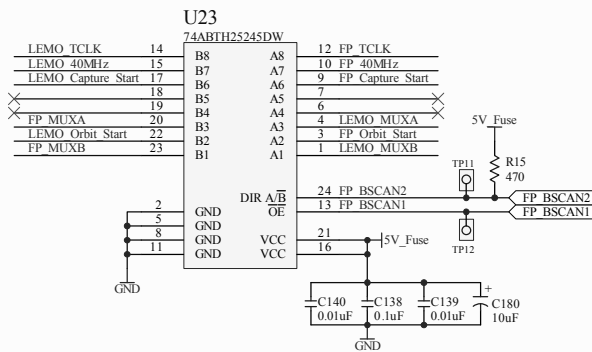
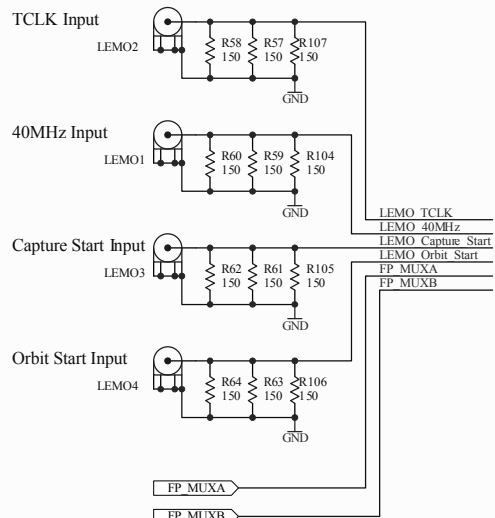


DAB64x: Delay Lines

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| Revision 0 | Drawing #: | | TRUMF 4004 Wesbrook Mall Vancouver, B.C. Canada V6T 2A3 | Cannot open file G:\AHW\PR OTEL\SCH LIB\T.DIB\IM |
| | Sheet #: 2 of 13 | Size: A | | |
| | Drawn by: TRIUMF | Date: 03/06/2004 | | |
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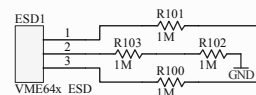
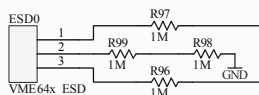


| DAB64x: Flash Configuration Memory | | | | | |
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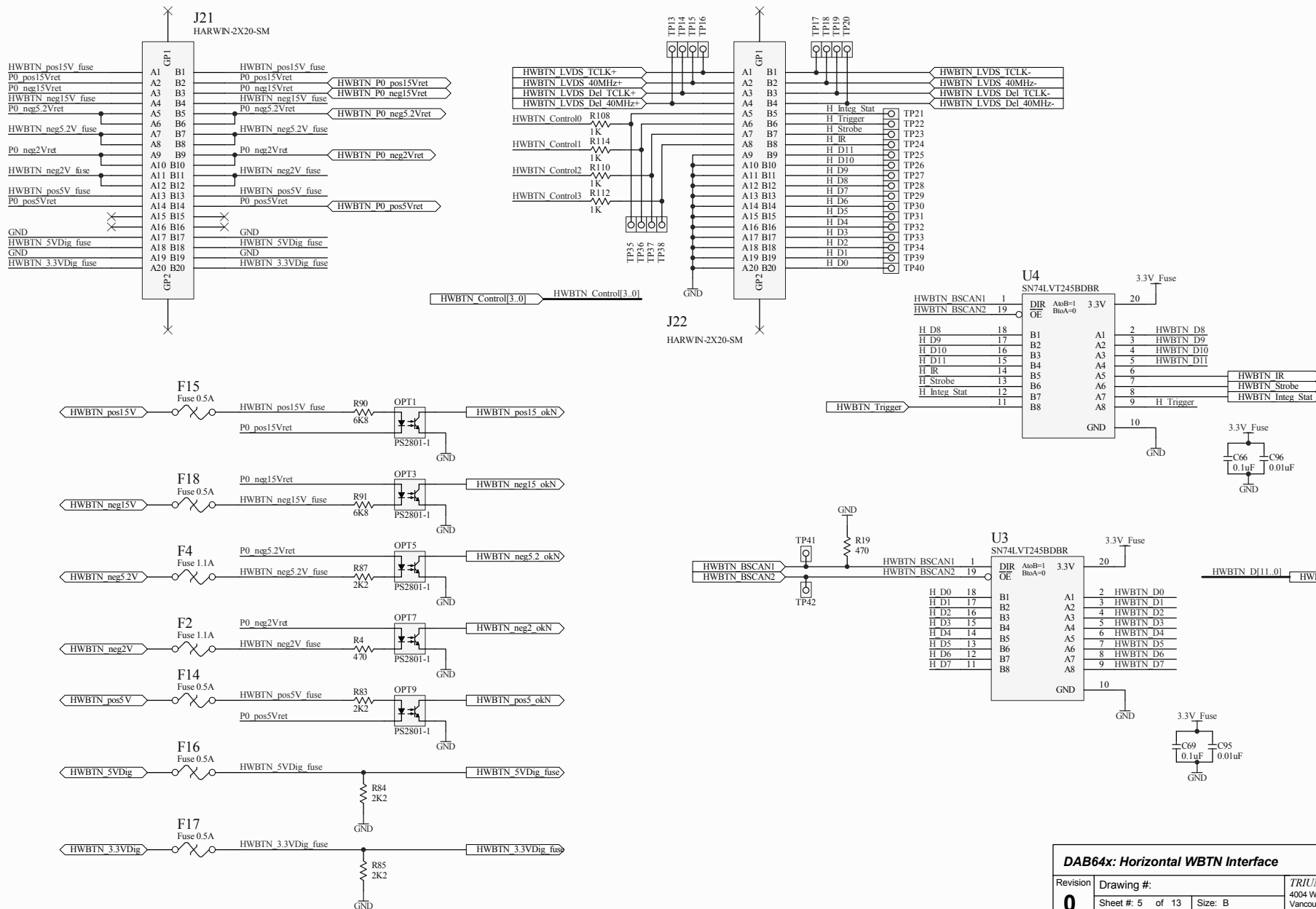
VME Access

ESD Front Panel Discharge Resistors

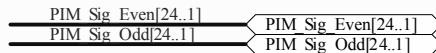
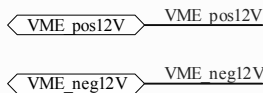
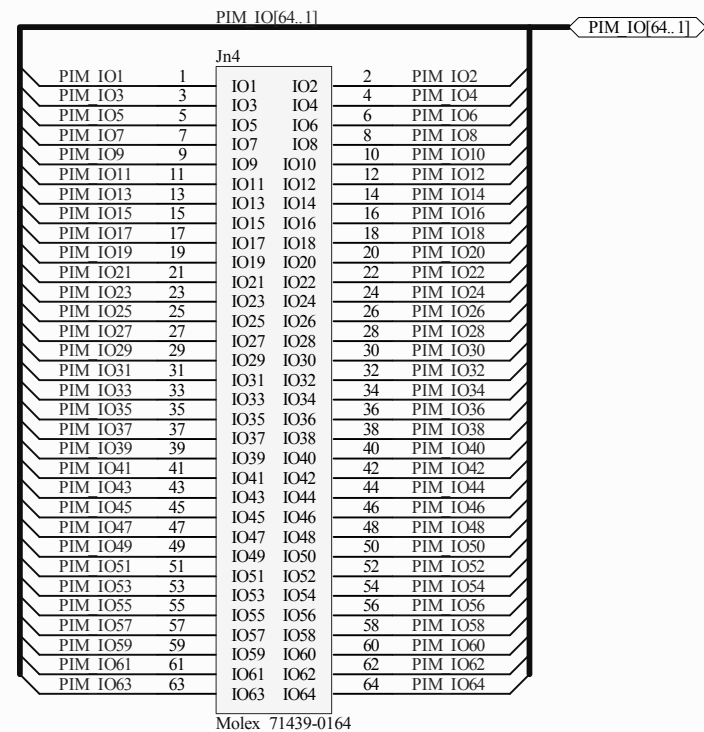
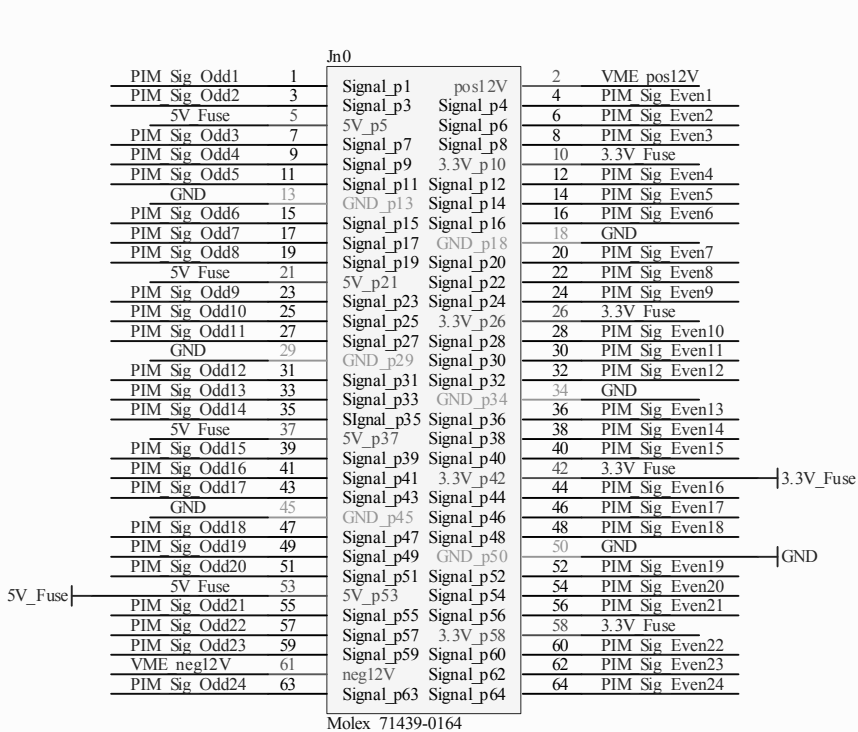
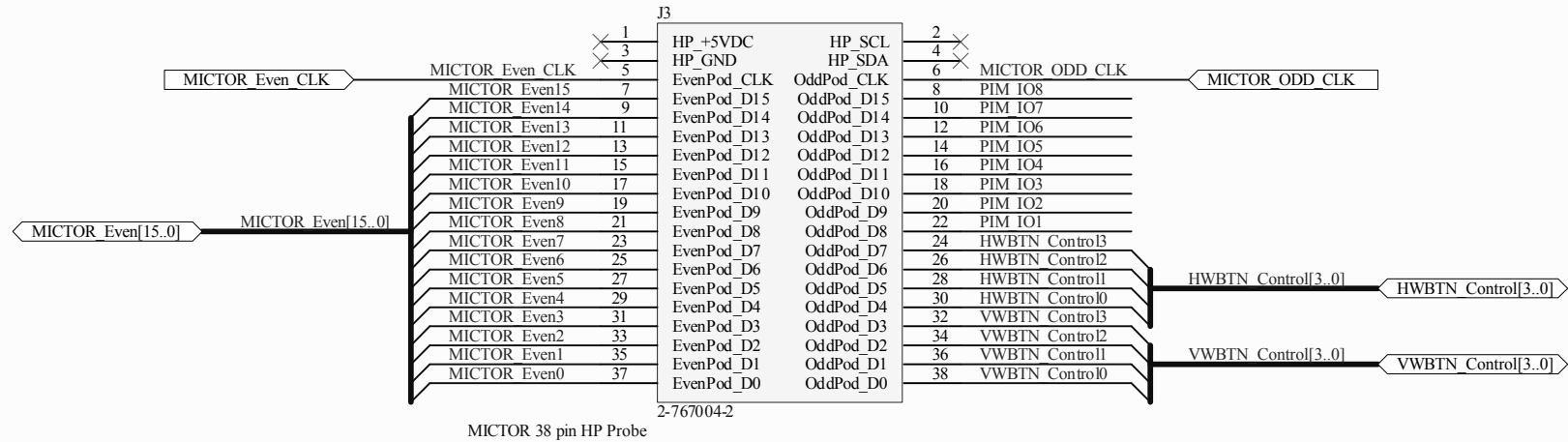


DAB64x: Front Panel Display and Signal I/O

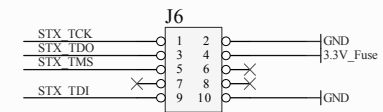
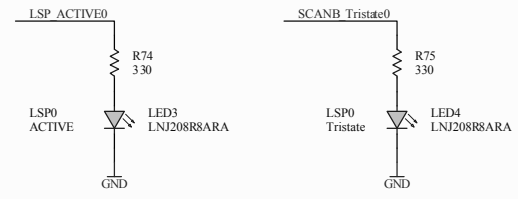
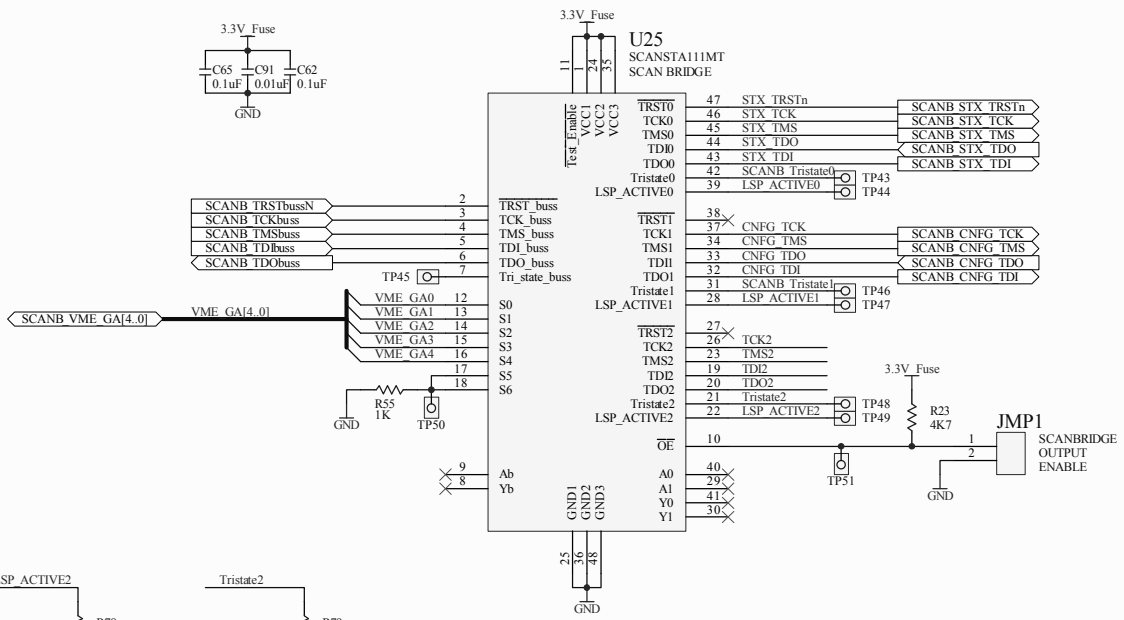
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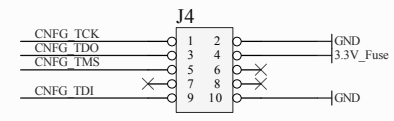
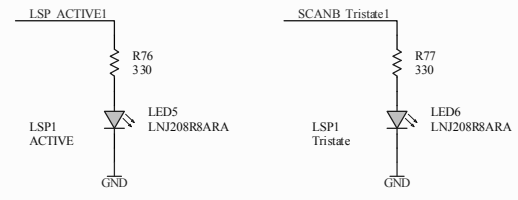
| DAB64x: Horizontal WBTN Interface | | | |
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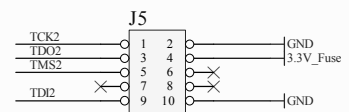
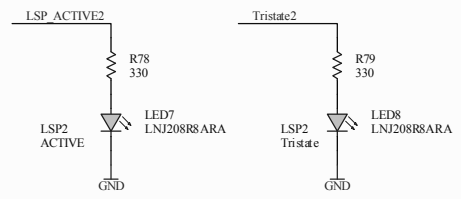
| DAB64x: MICTOR Connector / PIM Mezzanine Connectors | | | |
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| Revision | Drawing #: | | TRUMF |
| 0 | Sheet #: 6 of 13 | | 4004 Wesbrook Mall |
| | Size: A | | Vancouver, B.C. |
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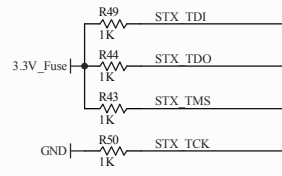
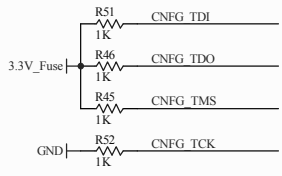
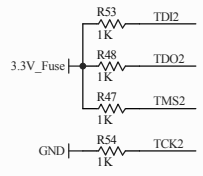
JTAG->STRATIX



JTAG->Config Flash



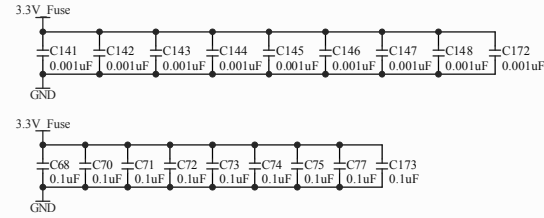
JTAG->Scan Chain2



| DAB64x: JTAG Interface / Scanbridge | | | |
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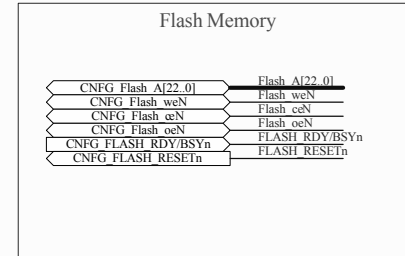
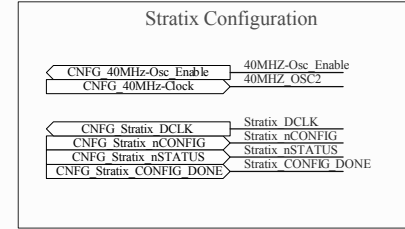
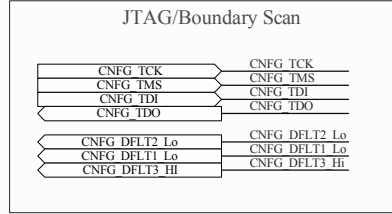
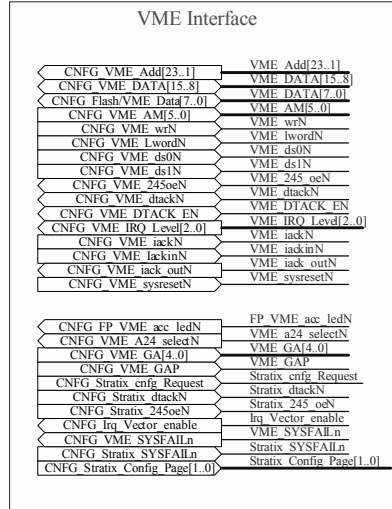
| U100A EPM3256ATC144-10 | | | |
|---------------------------|-----------|-------------|-------|
| VME Add17 | 1 | IO LabA | |
| VME Add16 | 2 | IO LabA | |
| | GND | 3 | GNDIO |
| CNFG TDI | 4 | TDI/IO LabB | |
| FP VME acc ledN | 5 | IO LabB | |
| VME A24 selectN | 6 | IO LabB | |
| 40MHz-Osc Enable | 7 | IO LabB | |
| Stratix cnfg Request | 8 | IO LabB | |
| Stratix 245 oeN | 9 | IO LabB | |
| VME IRQ Level0 | 10 | IO LabB | |
| VME IRQ Level2 | 11 | IO LabB | |
| VME DATA15 | 12 | IO LabF | |
| | GND | 13 | GNDIO |
| VME IRQ Level1 | 14 | IO LabF | |
| CNFG DFLT1 Lo | 15 | IO LabF | |
| CNFG DFLT3 HI | 16 | IO LabF | |
| | GND | 17 | GNDIO |
| CNFG DFLT2 Lo | 18 | IO LabF | |
| VME DATA14 | 19 | IO LabF | |
| CNFG TMS | 20 | IO LabF | |
| Iq Vector enable | 21 | IO LabG | |
| Stratix dtackN | 22 | IO LabG | |
| Stratix Config Page1 | 23 | IO LabG | |
| | 3.3V Fuse | 24 | VCCIO |
| Stratix Config Page0 | 25 | IO LabG | |
| | GND | 26 | GNDIO |
| Stratix SYSFAILn | 27 | IO LabG | |
| Stratix nSTATUS | 28 | IO LabG | |
| Stratix nCONFIG | 29 | IO LabC | |
| Stratix DCLK | 30 | IO LabC | |
| VME Add3 | 31 | IO LabC | |
| VME Add4 | 32 | IO LabC | |
| | GND | 33 | GNDIO |
| VME DATA13 | 34 | IO LabC | |
| VME DATA12 | 35 | IO LabC | |
| VME DATA11 | 36 | IO LabC | |

| U100B EPM3256ATC144-10 | | | |
|---------------------------|-----------|---------|--------|
| VME AM3 | 37 | IO LabD | |
| VME AM2 | 38 | IO LabD | |
| VME AM4 | 39 | IO LabD | |
| VME wrN | 40 | IO LabD | |
| VME AM5 | 41 | IO LabD | |
| VME LwordN | 42 | IO LabD | |
| VME Add23 | 43 | IO LabD | |
| VME ds1N | 44 | IO LabD | |
| VME ds0N | 45 | IO LabD | |
| VME Add22 | 46 | IO LabH | |
| VME Add21 | 47 | IO LabH | |
| VME Add20 | 48 | IO LabH | |
| VME Add19 | 49 | IO LabH | |
| | 3.3V Fuse | 50 | VCCIO |
| | 3.3V Fuse | 51 | VCCINT |
| | GND | 52 | GNDINT |
| VME AM0 | 53 | IO LabH | |
| VME AM1 | 54 | IO LabH | |
| VME Add15 | 55 | IO LabL | |
| VME Add14 | 56 | IO LabL | |
| | GND | 57 | GNDINT |
| | 3.3V Fuse | 58 | VCCINT |
| | GND | 59 | GNDIO |
| VME Add13 | 60 | IO LabL | |
| VME Add12 | 61 | IO LabL | |
| VME Add11 | 62 | IO LabL | |
| VME Add10 | 63 | IO LabL | |
| | GND | 64 | GNDIO |
| VME Add9 | 65 | IO LabL | |
| VME Add18 | 66 | IO LabP | |
| VME Add8 | 67 | IO LabP | |
| VME Add7 | 68 | IO LabP | |
| VME Add6 | 69 | IO LabP | |
| VME Add5 | 70 | IO LabP | |
| VME DATA9 | 71 | IO LabP | |
| VME DATA8 | 72 | IO LabP | |



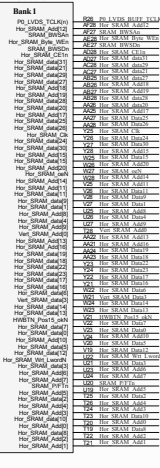
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| VME DATA10 | 74 | IO LabO | |
| | 3.3V Fuse | 75 | VCCIO |
| | GND | 76 | GNDIO |
| VME Add1 | 77 | IO LabO | |
| VME iackN | 78 | IO LabO | |
| VME iackinN | 79 | IO LabO | |
| VME iack outN | 80 | IO LabO | |
| VME 245 oeN | 81 | IO LabO | |
| VME dackN | 82 | IO LabK | |
| VME SYSFAILn | 83 | IO LabK | |
| | GND | 84 | IO LabK |
| VME DTACK EN | 85 | GNDIO | |
| Flash A0 | 86 | IO LabK | |
| Flash ceN | 87 | IO LabK | |
| CNFG TCK | 88 | IO LabK | |
| CNFG TDI | 89 | TCK/IO LabK | |
| VME GAP | 90 | IO LabJ | |
| Flash oeN | 91 | IO LabJ | |
| VME DATA0 | 92 | IO LabJ | |
| VME DATA1 | 93 | IO LabJ | |
| | GND | 94 | IO LabJ |
| | 3.3V Fuse | 95 | GNDIO |
| VME DATA2 | 96 | VCCIO | |
| VME DATA3 | 97 | IO LabJ | |
| Flash A21 | 98 | IO LabN | |
| VME DATA4 | 99 | IO LabN | |
| VME DATA5 | 100 | IO LabN | |
| VME DATA6 | 101 | IO LabN | |
| VME DATA7 | 102 | IO LabN | |
| VME GA4 | 103 | IO LabN | |
| CNFG TDO | 104 | IO LabN | |
| | GND | 105 | TD0/IO LabN |
| Flash A10 | 106 | IO LabM | |
| Flash A19 | 107 | IO LabM | |
| VME GA3 | 108 | IO LabM | |

| U100D EPM3256ATC144-10 | | | |
|---------------------------|-----------|-------------|--------|
| Flash A20 | 109 | VCCIO | |
| Flash A17 | 110 | IO LabM | |
| Flash A22 | 111 | IO LabM | |
| Flash A16 | 112 | IO LabM | |
| Flash A15 | 113 | IO LabM | |
| | GND | 114 | GNDIO |
| | 3.3V Fuse | 115 | VCCIO |
| Flash A14 | 116 | IO LabL | |
| Flash A13 | 117 | IO LabL | |
| Flash A12 | 118 | IO LabL | |
| FLASH RESEIn | 119 | IO LabL | |
| VME GA2 | 120 | IO LabL | |
| VME GA1 | 121 | IO LabL | |
| VME GA0 | 122 | IO LabL | |
| | 3.3V Fuse | 123 | VCCINT |
| | GND | 124 | GNDINT |
| 40MHZ_OSC2 | 125 | GCLK1/INPUT | |
| Stratix CONFIG DONE | 126 | OE1/INPUT | |
| VME sysresetN | 127 | GCLRn/INPUT | |
| FLASH RDY/BSYn | 128 | GCLK2/INPUT | |
| | GND | 129 | GNDINT |
| | 3.3V Fuse | 130 | VCCINT |
| Flash A11 | 131 | IO LabE | |
| Flash A9 | 132 | IO LabE | |
| Flash A8 | 133 | IO LabE | |
| Flash weN | 134 | IO LabE | |
| | GND | 135 | GNDIO |
| Flash A18 | 136 | IO LabE | |
| Flash A7 | 137 | IO LabE | |
| Flash A6 | 138 | IO LabE | |
| Flash A5 | 139 | IO LabE | |
| Flash A4 | 140 | IO LabA | |
| Flash A3 | 141 | IO LabA | |
| Flash A2 | 142 | IO LabA | |
| Flash A1 | 143 | IO LabA | |
| | 3.3V Fuse | 144 | VCCIO |

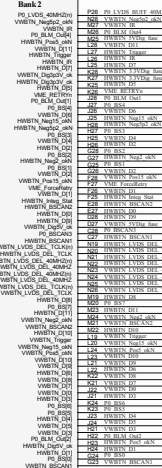


| DAB64x: Stratix Configuration Controller | | | | |
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| | Size: | B | Vancouver, B.C. | ROTEL\SC |
| | Drawn by: | Daryl Bishop | Canada | H. L. B. TRIM |
| | Date: | 03/06/2004 | V6T 2A3 | |
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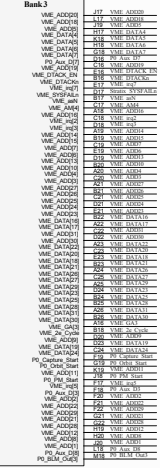
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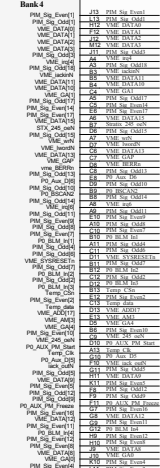
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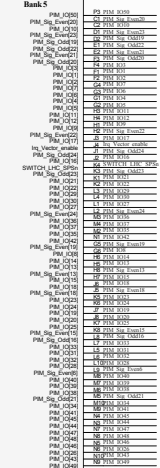
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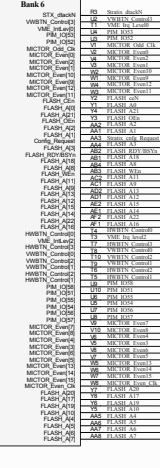
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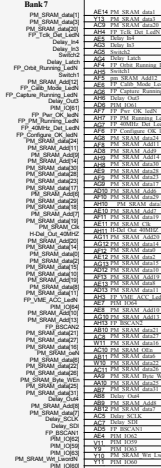
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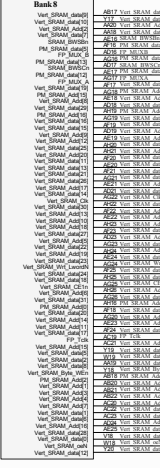
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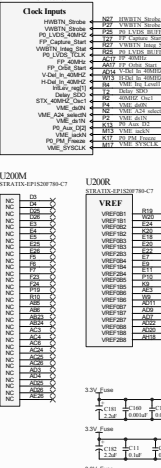
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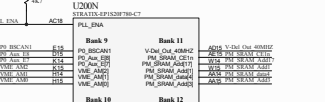
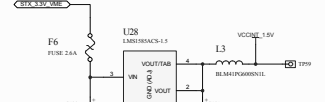
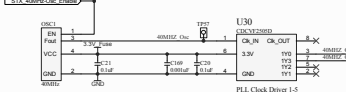
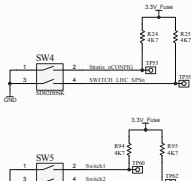
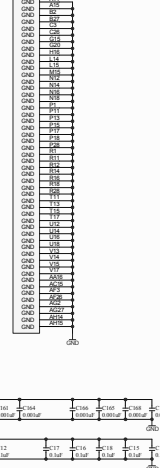
U200H STRATIX EP12K1070-C7



U200I STRATIX EP12K1070-C7



U200K STRATIX EP12K1070-C7



Stratix Configure table with parameters like SW4, SW5, SW6, SW7, SW8, SW9, SW10, SW11, SW12, SW13, SW14, SW15, SW16, SW17, SW18, SW19, SW20, SW21, SW22, SW23, SW24, SW25, SW26, SW27, SW28, SW29, SW30, SW31, SW32, SW33, SW34, SW35, SW36, SW37, SW38, SW39, SW40, SW41, SW42, SW43, SW44, SW45, SW46, SW47, SW48, SW49, SW50, SW51, SW52, SW53, SW54, SW55, SW56, SW57, SW58, SW59, SW60, SW61, SW62, SW63, SW64, SW65, SW66, SW67, SW68, SW69, SW70, SW71, SW72, SW73, SW74, SW75, SW76, SW77, SW78, SW79, SW80, SW81, SW82, SW83, SW84, SW85, SW86, SW87, SW88, SW89, SW90, SW91, SW92, SW93, SW94, SW95, SW96, SW97, SW98, SW99, SW100.

FIAC table with parameters like FIAC0, FIAC1, FIAC2, FIAC3, FIAC4, FIAC5, FIAC6, FIAC7, FIAC8, FIAC9, FIAC10, FIAC11, FIAC12, FIAC13, FIAC14, FIAC15, FIAC16, FIAC17, FIAC18, FIAC19, FIAC20, FIAC21, FIAC22, FIAC23, FIAC24, FIAC25, FIAC26, FIAC27, FIAC28, FIAC29, FIAC30, FIAC31, FIAC32, FIAC33, FIAC34, FIAC35, FIAC36, FIAC37, FIAC38, FIAC39, FIAC40, FIAC41, FIAC42, FIAC43, FIAC44, FIAC45, FIAC46, FIAC47, FIAC48, FIAC49, FIAC50, FIAC51, FIAC52, FIAC53, FIAC54, FIAC55, FIAC56, FIAC57, FIAC58, FIAC59, FIAC60, FIAC61, FIAC62, FIAC63, FIAC64, FIAC65, FIAC66, FIAC67, FIAC68, FIAC69, FIAC70, FIAC71, FIAC72, FIAC73, FIAC74, FIAC75, FIAC76, FIAC77, FIAC78, FIAC79, FIAC80, FIAC81, FIAC82, FIAC83, FIAC84, FIAC85, FIAC86, FIAC87, FIAC88, FIAC89, FIAC90, FIAC91, FIAC92, FIAC93, FIAC94, FIAC95, FIAC96, FIAC97, FIAC98, FIAC99, FIAC100.

Front Panel Signals table with parameters like FP_SIG0, FP_SIG1, FP_SIG2, FP_SIG3, FP_SIG4, FP_SIG5, FP_SIG6, FP_SIG7, FP_SIG8, FP_SIG9, FP_SIG10, FP_SIG11, FP_SIG12, FP_SIG13, FP_SIG14, FP_SIG15, FP_SIG16, FP_SIG17, FP_SIG18, FP_SIG19, FP_SIG20, FP_SIG21, FP_SIG22, FP_SIG23, FP_SIG24, FP_SIG25, FP_SIG26, FP_SIG27, FP_SIG28, FP_SIG29, FP_SIG30, FP_SIG31, FP_SIG32, FP_SIG33, FP_SIG34, FP_SIG35, FP_SIG36, FP_SIG37, FP_SIG38, FP_SIG39, FP_SIG40, FP_SIG41, FP_SIG42, FP_SIG43, FP_SIG44, FP_SIG45, FP_SIG46, FP_SIG47, FP_SIG48, FP_SIG49, FP_SIG50, FP_SIG51, FP_SIG52, FP_SIG53, FP_SIG54, FP_SIG55, FP_SIG56, FP_SIG57, FP_SIG58, FP_SIG59, FP_SIG60, FP_SIG61, FP_SIG62, FP_SIG63, FP_SIG64, FP_SIG65, FP_SIG66, FP_SIG67, FP_SIG68, FP_SIG69, FP_SIG70, FP_SIG71, FP_SIG72, FP_SIG73, FP_SIG74, FP_SIG75, FP_SIG76, FP_SIG77, FP_SIG78, FP_SIG79, FP_SIG80, FP_SIG81, FP_SIG82, FP_SIG83, FP_SIG84, FP_SIG85, FP_SIG86, FP_SIG87, FP_SIG88, FP_SIG89, FP_SIG90, FP_SIG91, FP_SIG92, FP_SIG93, FP_SIG94, FP_SIG95, FP_SIG96, FP_SIG97, FP_SIG98, FP_SIG99, FP_SIG100.

Front Panel Display table with parameters like FP_DISP0, FP_DISP1, FP_DISP2, FP_DISP3, FP_DISP4, FP_DISP5, FP_DISP6, FP_DISP7, FP_DISP8, FP_DISP9, FP_DISP10, FP_DISP11, FP_DISP12, FP_DISP13, FP_DISP14, FP_DISP15, FP_DISP16, FP_DISP17, FP_DISP18, FP_DISP19, FP_DISP20, FP_DISP21, FP_DISP22, FP_DISP23, FP_DISP24, FP_DISP25, FP_DISP26, FP_DISP27, FP_DISP28, FP_DISP29, FP_DISP30, FP_DISP31, FP_DISP32, FP_DISP33, FP_DISP34, FP_DISP35, FP_DISP36, FP_DISP37, FP_DISP38, FP_DISP39, FP_DISP40, FP_DISP41, FP_DISP42, FP_DISP43, FP_DISP44, FP_DISP45, FP_DISP46, FP_DISP47, FP_DISP48, FP_DISP49, FP_DISP50, FP_DISP51, FP_DISP52, FP_DISP53, FP_DISP54, FP_DISP55, FP_DISP56, FP_DISP57, FP_DISP58, FP_DISP59, FP_DISP60, FP_DISP61, FP_DISP62, FP_DISP63, FP_DISP64, FP_DISP65, FP_DISP66, FP_DISP67, FP_DISP68, FP_DISP69, FP_DISP70, FP_DISP71, FP_DISP72, FP_DISP73, FP_DISP74, FP_DISP75, FP_DISP76, FP_DISP77, FP_DISP78, FP_DISP79, FP_DISP80, FP_DISP81, FP_DISP82, FP_DISP83, FP_DISP84, FP_DISP85, FP_DISP86, FP_DISP87, FP_DISP88, FP_DISP89, FP_DISP90, FP_DISP91, FP_DISP92, FP_DISP93, FP_DISP94, FP_DISP95, FP_DISP96, FP_DISP97, FP_DISP98, FP_DISP99, FP_DISP100.

4-Channel Delay Line table with parameters like DELAY0, DELAY1, DELAY2, DELAY3, DELAY4, DELAY5, DELAY6, DELAY7, DELAY8, DELAY9, DELAY10, DELAY11, DELAY12, DELAY13, DELAY14, DELAY15, DELAY16, DELAY17, DELAY18, DELAY19, DELAY20, DELAY21, DELAY22, DELAY23, DELAY24, DELAY25, DELAY26, DELAY27, DELAY28, DELAY29, DELAY30, DELAY31, DELAY32, DELAY33, DELAY34, DELAY35, DELAY36, DELAY37, DELAY38, DELAY39, DELAY40, DELAY41, DELAY42, DELAY43, DELAY44, DELAY45, DELAY46, DELAY47, DELAY48, DELAY49, DELAY50, DELAY51, DELAY52, DELAY53, DELAY54, DELAY55, DELAY56, DELAY57, DELAY58, DELAY59, DELAY60, DELAY61, DELAY62, DELAY63, DELAY64, DELAY65, DELAY66, DELAY67, DELAY68, DELAY69, DELAY70, DELAY71, DELAY72, DELAY73, DELAY74, DELAY75, DELAY76, DELAY77, DELAY78, DELAY79, DELAY80, DELAY81, DELAY82, DELAY83, DELAY84, DELAY85, DELAY86, DELAY87, DELAY88, DELAY89, DELAY90, DELAY91, DELAY92, DELAY93, DELAY94, DELAY95, DELAY96, DELAY97, DELAY98, DELAY99, DELAY100.

Horizontal WB1N table with parameters like HB_WB1N0, HB_WB1N1, HB_WB1N2, HB_WB1N3, HB_WB1N4, HB_WB1N5, HB_WB1N6, HB_WB1N7, HB_WB1N8, HB_WB1N9, HB_WB1N10, HB_WB1N11, HB_WB1N12, HB_WB1N13, HB_WB1N14, HB_WB1N15, HB_WB1N16, HB_WB1N17, HB_WB1N18, HB_WB1N19, HB_WB1N20, HB_WB1N21, HB_WB1N22, HB_WB1N23, HB_WB1N24, HB_WB1N25, HB_WB1N26, HB_WB1N27, HB_WB1N28, HB_WB1N29, HB_WB1N30, HB_WB1N31, HB_WB1N32, HB_WB1N33, HB_WB1N34, HB_WB1N35, HB_WB1N36, HB_WB1N37, HB_WB1N38, HB_WB1N39, HB_WB1N40, HB_WB1N41, HB_WB1N42, HB_WB1N43, HB_WB1N44, HB_WB1N45, HB_WB1N46, HB_WB1N47, HB_WB1N48, HB_WB1N49, HB_WB1N50, HB_WB1N51, HB_WB1N52, HB_WB1N53, HB_WB1N54, HB_WB1N55, HB_WB1N56, HB_WB1N57, HB_WB1N58, HB_WB1N59, HB_WB1N60, HB_WB1N61, HB_WB1N62, HB_WB1N63, HB_WB1N64, HB_WB1N65, HB_WB1N66, HB_WB1N67, HB_WB1N68, HB_WB1N69, HB_WB1N70, HB_WB1N71, HB_WB1N72, HB_WB1N73, HB_WB1N74, HB_WB1N75, HB_WB1N76, HB_WB1N77, HB_WB1N78, HB_WB1N79, HB_WB1N80, HB_WB1N81, HB_WB1N82, HB_WB1N83, HB_WB1N84, HB_WB1N85, HB_WB1N86, HB_WB1N87, HB_WB1N88, HB_WB1N89, HB_WB1N90, HB_WB1N91, HB_WB1N92, HB_WB1N93, HB_WB1N94, HB_WB1N95, HB_WB1N96, HB_WB1N97, HB_WB1N98, HB_WB1N99, HB_WB1N100.

Vertical WB1N table with parameters like VB_WB1N0, VB_WB1N1, VB_WB1N2, VB_WB1N3, VB_WB1N4, VB_WB1N5, VB_WB1N6, VB_WB1N7, VB_WB1N8, VB_WB1N9, VB_WB1N10, VB_WB1N11, VB_WB1N12, VB_WB1N13, VB_WB1N14, VB_WB1N15, VB_WB1N16, VB_WB1N17, VB_WB1N18, VB_WB1N19, VB_WB1N20, VB_WB1N21, VB_WB1N22, VB_WB1N23, VB_WB1N24, VB_WB1N25, VB_WB1N26, VB_WB1N27, VB_WB1N28, VB_WB1N29, VB_WB1N30, VB_WB1N31, VB_WB1N32, VB_WB1N33, VB_WB1N34, VB_WB1N35, VB_WB1N36, VB_WB1N37, VB_WB1N38, VB_WB1N39, VB_WB1N40, VB_WB1N41, VB_WB1N42, VB_WB1N43, VB_WB1N44, VB_WB1N45, VB_WB1N46, VB_WB1N47, VB_WB1N48, VB_WB1N49, VB_WB1N50, VB_WB1N51, VB_WB1N52, VB_WB1N53, VB_WB1N54, VB_WB1N55, VB_WB1N56, VB_WB1N57, VB_WB1N58, VB_WB1N59, VB_WB1N60, VB_WB1N61, VB_WB1N62, VB_WB1N63, VB_WB1N64, VB_WB1N65, VB_WB1N66, VB_WB1N67, VB_WB1N68, VB_WB1N69, VB_WB1N70, VB_WB1N71, VB_WB1N72, VB_WB1N73, VB_WB1N74, VB_WB1N75, VB_WB1N76, VB_WB1N77, VB_WB1N78, VB_WB1N79, VB_WB1N80, VB_WB1N81, VB_WB1N82, VB_WB1N83, VB_WB1N84, VB_WB1N85, VB_WB1N86, VB_WB1N87, VB_WB1N88, VB_WB1N89, VB_WB1N90, VB_WB1N91, VB_WB1N92, VB_WB1N93, VB_WB1N94, VB_WB1N95, VB_WB1N96, VB_WB1N97, VB_WB1N98, VB_WB1N99, VB_WB1N100.

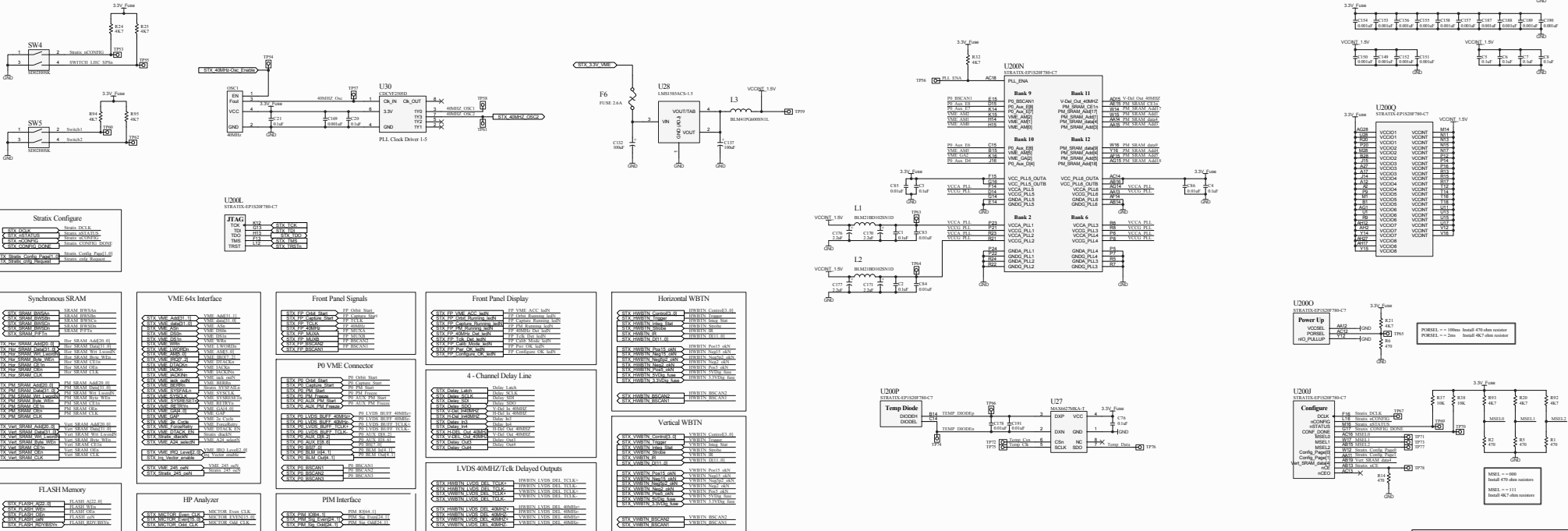
PO VME Connector table with parameters like PO_VME0, PO_VME1, PO_VME2, PO_VME3, PO_VME4, PO_VME5, PO_VME6, PO_VME7, PO_VME8, PO_VME9, PO_VME10, PO_VME11, PO_VME12, PO_VME13, PO_VME14, PO_VME15, PO_VME16, PO_VME17, PO_VME18, PO_VME19, PO_VME20, PO_VME21, PO_VME22, PO_VME23, PO_VME24, PO_VME25, PO_VME26, PO_VME27, PO_VME28, PO_VME29, PO_VME30, PO_VME31, PO_VME32, PO_VME33, PO_VME34, PO_VME35, PO_VME36, PO_VME37, PO_VME38, PO_VME39, PO_VME40, PO_VME41, PO_VME42, PO_VME43, PO_VME44, PO_VME45, PO_VME46, PO_VME47, PO_VME48, PO_VME49, PO_VME50, PO_VME51, PO_VME52, PO_VME53, PO_VME54, PO_VME55, PO_VME56, PO_VME57, PO_VME58, PO_VME59, PO_VME60, PO_VME61, PO_VME62, PO_VME63, PO_VME64, PO_VME65, PO_VME66, PO_VME67, PO_VME68, PO_VME69, PO_VME70, PO_VME71, PO_VME72, PO_VME73, PO_VME74, PO_VME75, PO_VME76, PO_VME77, PO_VME78, PO_VME79, PO_VME80, PO_VME81, PO_VME82, PO_VME83, PO_VME84, PO_VME85, PO_VME86, PO_VME87, PO_VME88, PO_VME89, PO_VME90, PO_VME91, PO_VME92, PO_VME93, PO_VME94, PO_VME95, PO_VME96, PO_VME97, PO_VME98, PO_VME99, PO_VME100.

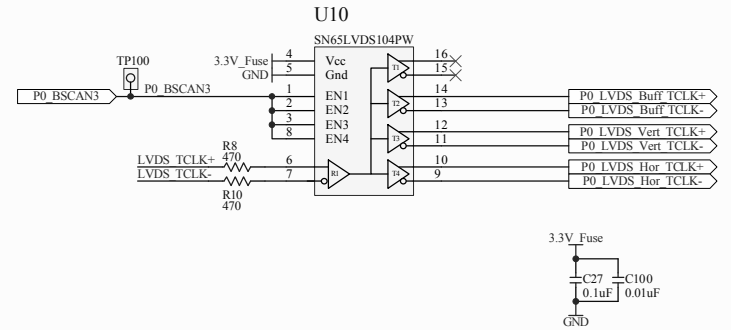
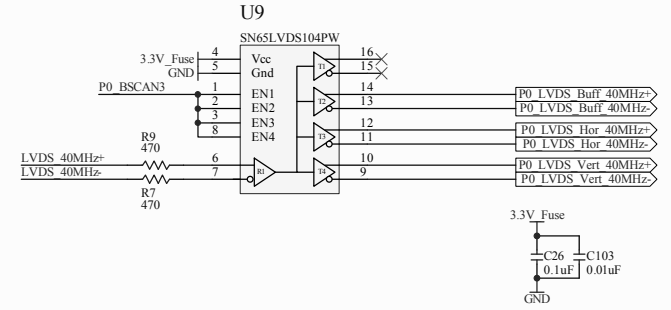
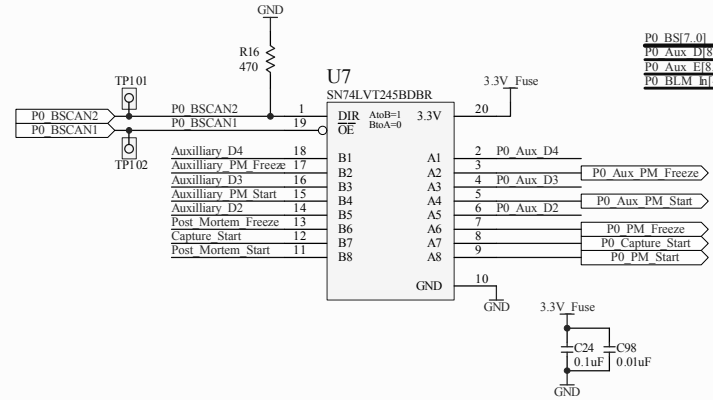
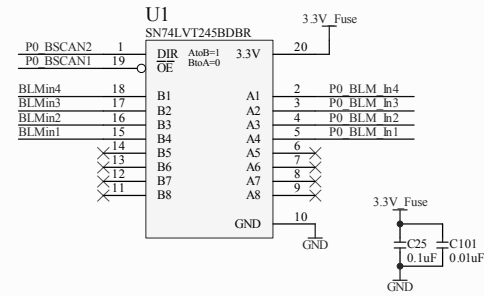
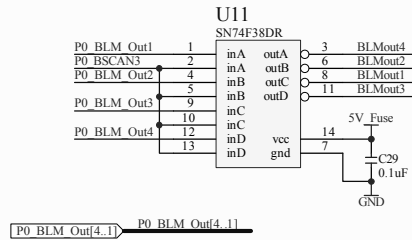
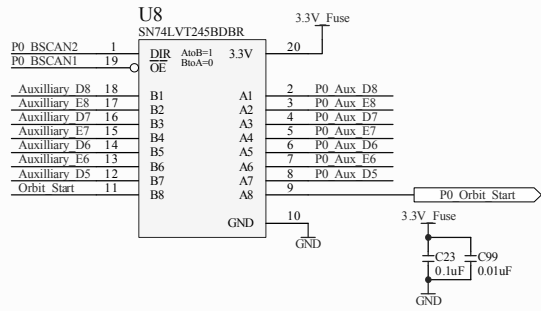
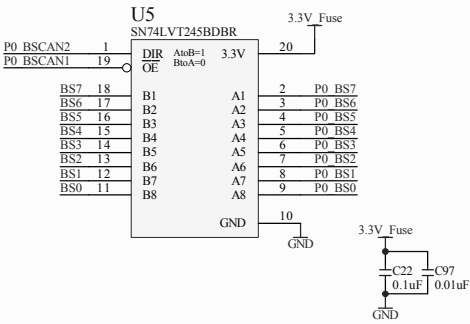
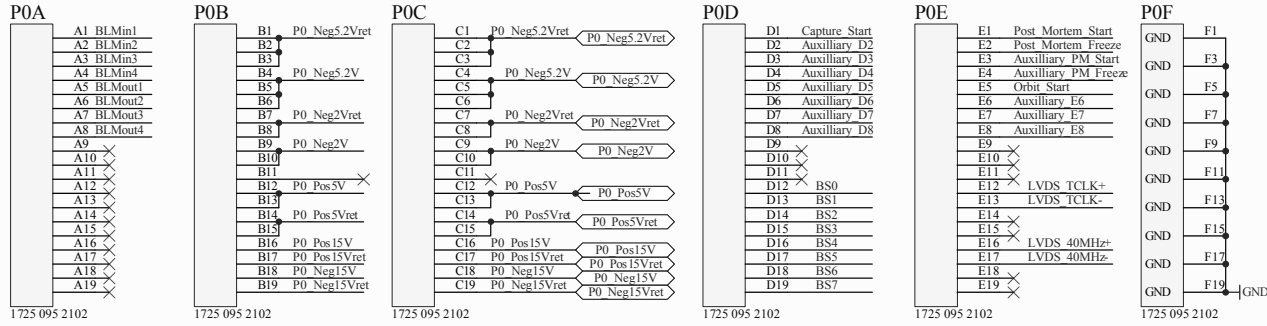
LVDS 40MHz/Tclk Delayed Outputs table with parameters like LVDS_OUT0, LVDS_OUT1, LVDS_OUT2, LVDS_OUT3, LVDS_OUT4, LVDS_OUT5, LVDS_OUT6, LVDS_OUT7, LVDS_OUT8, LVDS_OUT9, LVDS_OUT10, LVDS_OUT11, LVDS_OUT12, LVDS_OUT13, LVDS_OUT14, LVDS_OUT15, LVDS_OUT16, LVDS_OUT17, LVDS_OUT18, LVDS_OUT19, LVDS_OUT20, LVDS_OUT21, LVDS_OUT22, LVDS_OUT23, LVDS_OUT24, LVDS_OUT25, LVDS_OUT26, LVDS_OUT27, LVDS_OUT28, LVDS_OUT29, LVDS_OUT30, LVDS_OUT31, LVDS_OUT32, LVDS_OUT33, LVDS_OUT34, LVDS_OUT35, LVDS_OUT36, LVDS_OUT37, LVDS_OUT38, LVDS_OUT39, LVDS_OUT40, LVDS_OUT41, LVDS_OUT42, LVDS_OUT43, LVDS_OUT44, LVDS_OUT45, LVDS_OUT46, LVDS_OUT47, LVDS_OUT48, LVDS_OUT49, LVDS_OUT50, LVDS_OUT51, LVDS_OUT52, LVDS_OUT53, LVDS_OUT54, LVDS_OUT55, LVDS_OUT56, LVDS_OUT57, LVDS_OUT58, LVDS_OUT59, LVDS_OUT60, LVDS_OUT61, LVDS_OUT62, LVDS_OUT63, LVDS_OUT64, LVDS_OUT65, LVDS_OUT66, LVDS_OUT67, LVDS_OUT68, LVDS_OUT69, LVDS_OUT70, LVDS_OUT71, LVDS_OUT72, LVDS_OUT73, LVDS_OUT74, LVDS_OUT75, LVDS_OUT76, LVDS_OUT77, LVDS_OUT78, LVDS_OUT79, LVDS_OUT80, LVDS_OUT81, LVDS_OUT82, LVDS_OUT83, LVDS_OUT84, LVDS_OUT85, LVDS_OUT86, LVDS_OUT87, LVDS_OUT88, LVDS_OUT89, LVDS_OUT90, LVDS_OUT91, LVDS_OUT92, LVDS_OUT93, LVDS_OUT94, LVDS_OUT95, LVDS_OUT96, LVDS_OUT97, LVDS_OUT98, LVDS_OUT99, LVDS_OUT100.

FLASH Memory table with parameters like FLASH0, FLASH1, FLASH2, FLASH3, FLASH4, FLASH5, FLASH6, FLASH7, FLASH8, FLASH9, FLASH10, FLASH11, FLASH12, FLASH13, FLASH14, FLASH15, FLASH16, FLASH17, FLASH18, FLASH19, FLASH20, FLASH21, FLASH22, FLASH23, FLASH24, FLASH25, FLASH26, FLASH27, FLASH28, FLASH29, FLASH30, FLASH31, FLASH32, FLASH33, FLASH34, FLASH35, FLASH36, FLASH37, FLASH38, FLASH39, FLASH40, FLASH41, FLASH42, FLASH43, FLASH44, FLASH45, FLASH46, FLASH47, FLASH48, FLASH49, FLASH50, FLASH51, FLASH52, FLASH53, FLASH54, FLASH55, FLASH56, FLASH57, FLASH58, FLASH59, FLASH60, FLASH61, FLASH62, FLASH63, FLASH64, FLASH65, FLASH66, FLASH67, FLASH68, FLASH69, FLASH70, FLASH71, FLASH72, FLASH73, FLASH74, FLASH75, FLASH76, FLASH77, FLASH78, FLASH79, FLASH80, FLASH81, FLASH82, FLASH83, FLASH84, FLASH85, FLASH86, FLASH87, FLASH88, FLASH89, FLASH90, FLASH91, FLASH92, FLASH93, FLASH94, FLASH95, FLASH96, FLASH97, FLASH98, FLASH99, FLASH100.

HP Analyzer table with parameters like HP_ANALYZER0, HP_ANALYZER1, HP_ANALYZER2, HP_ANALYZER3, HP_ANALYZER4, HP_ANALYZER5, HP_ANALYZER6, HP_ANALYZER7, HP_ANALYZER8, HP_ANALYZER9, HP_ANALYZER10, HP_ANALYZER11, HP_ANALYZER12, HP_ANALYZER13, HP_ANALYZER14, HP_ANALYZER15, HP_ANALYZER16, HP_ANALYZER17, HP_ANALYZER18, HP_ANALYZER19, HP_ANALYZER20, HP_ANALYZER21, HP_ANALYZER22, HP_ANALYZER23, HP_ANALYZER24, HP_ANALYZER25, HP_ANALYZER26, HP_ANALYZER27, HP_ANALYZER28, HP_ANALYZER29, HP_ANALYZER30, HP_ANALYZER31, HP_ANALYZER32, HP_ANALYZER33, HP_ANALYZER34, HP_ANALYZER35, HP_ANALYZER36, HP_ANALYZER37, HP_ANALYZER38, HP_ANALYZER39, HP_ANALYZER40, HP_ANALYZER41, HP_ANALYZER42, HP_ANALYZER43, HP_ANALYZER44, HP_ANALYZER45, HP_ANALYZER46, HP_ANALYZER47, HP_ANALYZER48, HP_ANALYZER49, HP_ANALYZER50, HP_ANALYZER51, HP_ANALYZER52, HP_ANALYZER53, HP_ANALYZER54, HP_ANALYZER55, HP_ANALYZER56, HP_ANALYZER57, HP_ANALYZER58, HP_ANALYZER59, HP_ANALYZER60, HP_ANALYZER61, HP_ANALYZER62, HP_ANALYZER63, HP_ANALYZER64, HP_ANALYZER65, HP_ANALYZER66, HP_ANALYZER67, HP_ANALYZER68, HP_ANALYZER69, HP_ANALYZER70, HP_ANALYZER71, HP_ANALYZER72, HP_ANALYZER73, HP_ANALYZER74, HP_ANALYZER75, HP_ANALYZER76, HP_ANALYZER77, HP_ANALYZER78, HP_ANALYZER79, HP_ANALYZER80, HP_ANALYZER81, HP_ANALYZER82, HP_ANALYZER83, HP_ANALYZER84, HP_ANALYZER85, HP_ANALYZER86, HP_ANALYZER87, HP_ANALYZER88, HP_ANALYZER89, HP_ANALYZER90, HP_ANALYZER91, HP_ANALYZER92, HP_ANALYZER93, HP_ANALYZER94, HP_ANALYZER95, HP_ANALYZER96, HP_ANALYZER97, HP_ANALYZER98, HP_ANALYZER99, HP_ANALYZER100.

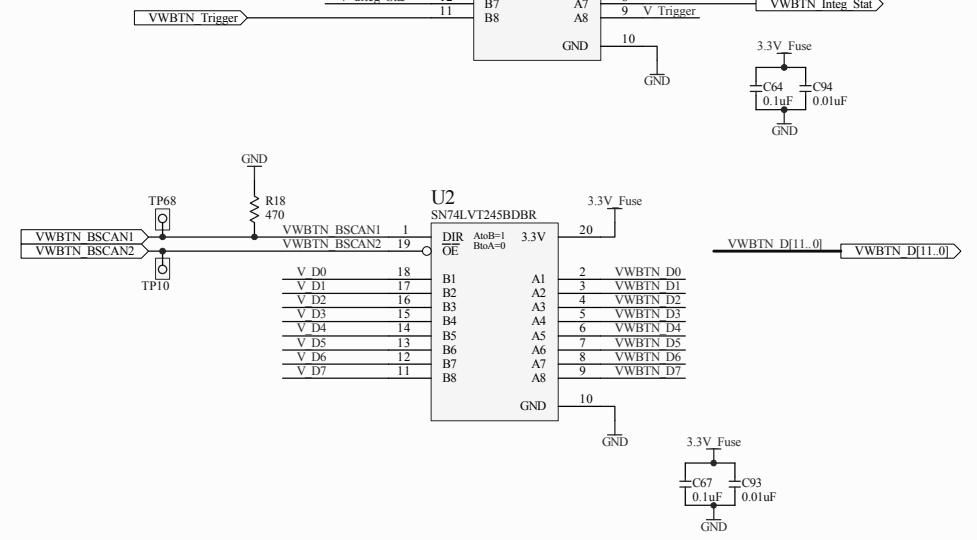
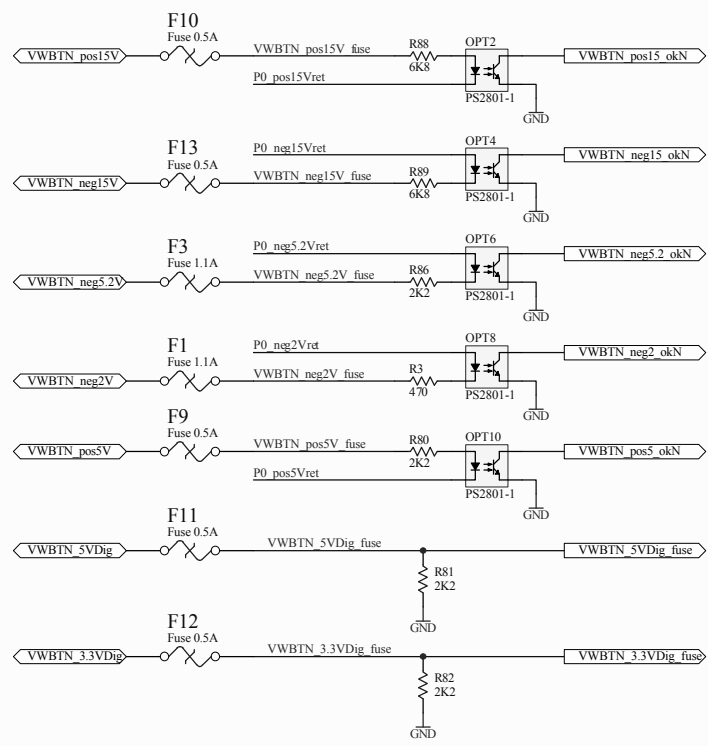
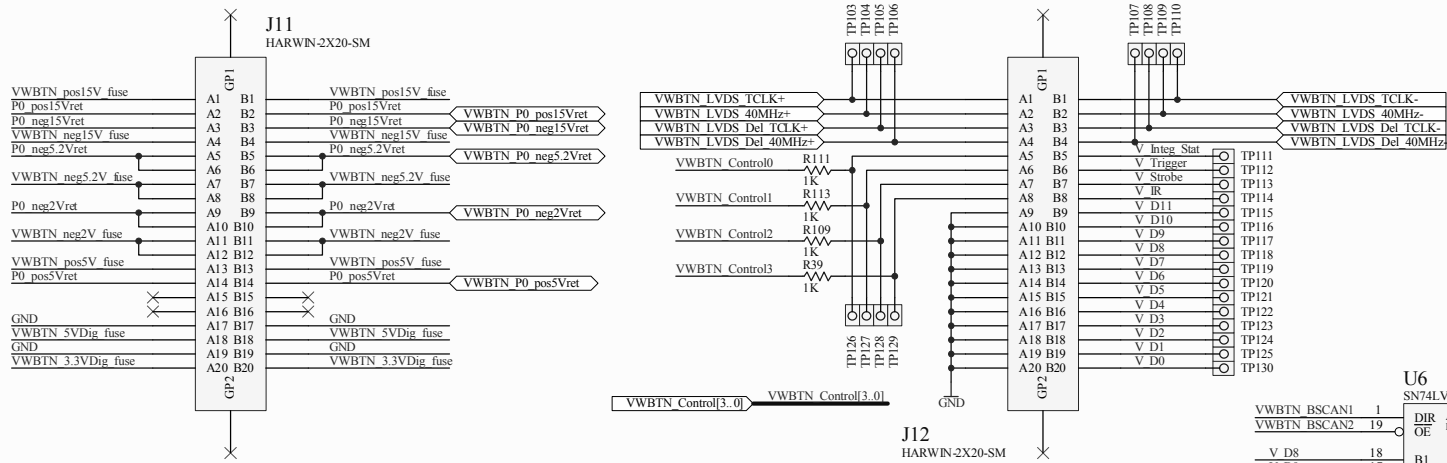
PIM Interface table with parameters like PIM_INTERFACE0, PIM_INTERFACE1, PIM_INTERFACE2, PIM_INTERFACE3, PIM_INTERFACE4, PIM_INTERFACE5, PIM_INTERFACE6, PIM_INTERFACE7, PIM_INTERFACE8, PIM_INTERFACE9, PIM_INTERFACE10, PIM_INTERFACE11, PIM_INTERFACE12, PIM_INTERFACE13, PIM_INTERFACE14, PIM_INTERFACE15, PIM_INTERFACE16, PIM_INTERFACE17, PIM_INTERFACE18, PIM_INTERFACE19, PIM_INTERFACE20, PIM_INTERFACE21, PIM_INTERFACE22, PIM_INTERFACE23, PIM_INTERFACE24, PIM_INTERFACE25, PIM_INTERFACE26, PIM_INTERFACE27, PIM_INTERFACE28, PIM_INTERFACE29, PIM_INTERFACE30, PIM_INTERFACE31, PIM_INTERFACE32, PIM_INTERFACE33, PIM_INTERFACE34, PIM_INTERFACE35, PIM_INTERFACE36, PIM_INTERFACE37, PIM_INTERFACE38, PIM_INTERFACE39, PIM_INTERFACE40, PIM_INTERFACE41, PIM_INTERFACE42, PIM_INTERFACE43, PIM_INTERFACE44, PIM_INTERFACE45, PIM_INTERFACE46, PIM_INTERFACE47, PIM_INTERFACE48, PIM_INTERFACE49, PIM_INTERFACE50, PIM_INTERFACE51, PIM_INTERFACE52, PIM_INTERFACE53, PIM_INTERFACE54, PIM_INTERFACE55, PIM_INTERFACE56, PIM_INTERFACE57, PIM_INTERFACE58, PIM_INTERFACE59, PIM_INTERFACE60, PIM_INTERFACE61, PIM_INTERFACE62, PIM_INTERFACE63, PIM_INTERFACE64, PIM_INTERFACE65, PIM_INTERFACE66, PIM_INTERFACE67, PIM_INTERFACE68, PIM_INTERFACE69, PIM_INTERFACE70, PIM_INTERFACE71, PIM_INTERFACE72, PIM_INTERFACE73, PIM_INTERFACE74, PIM_INTERFACE75, PIM_INTERFACE76, PIM_INTERFACE77, PIM_INTERFACE78, PIM_INTERFACE79, PIM_INTERFACE80, PIM_INTERFACE81, PIM_INTERFACE82, PIM_INTERFACE83, PIM_INTERFACE84, PIM_INTERFACE85, PIM_INTERFACE86, PIM_INTERFACE87, PIM_INTERFACE88, PIM_INTERFACE89, PIM_INTERFACE90, PIM_INTERFACE91, PIM_INTERFACE92, PIM_INTERFACE93, PIM_INTERFACE94, PIM_INTERFACE95, PIM_INTERFACE96, PIM_INTERFACE97, PIM_INTERFACE98, PIM_INTERFACE99, PIM_INTERFACE100.





DAB64x: VME P0 Interface

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| | Size: B | Vancouver, B.C. | ROTEL\SC |
| | Drawn by: Daryl Bishop | Canada | H.1.BR\TRII |
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| DAB64x: Vertical WBTN Interface | | | |
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| Revision: 0 | Drawing #: 13 of 13 | Size: B | TRUMF 4004 Westbrook Mall Vancouver, B.C. Canada V6T 2A3 |
| Drawn by: Daryl Bishop | Date: 03/06/2004 | | Cannot open file G:\AHWP\ROTEL\SCHEM\LIB\TRIM11.45.27 |
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