

**1310 nm Single-mode Transceiver (S1.1)**  
**2×5, LC Duplex Connector, 3.3 V**  
**155 Mbps ATM/SONET OC-3/SDH STM-1**



**Features**

- Compliant with 155 Mbps ATM and SONET OC-3 SDH STM-1 (S1.1)
- Industry standard 2×5 footprint
- LC duplex connector
- Single power supply 3.3 V
- Differential LVPECL inputs and outputs
- Compatible with solder and aqueous wash processes
- Class 1 laser product complies with EN 60825-1

**Description**

The TS3-0155-34S-P1-X transceivers utilizing 1310 nm MQW laser diodes are fully compliance to the 155 Mbps ATM and SONET OC-3 SDH STM-1 standard, and designed in industry standard 2×5 package with LC duplex connector. The transmitter section is a class 1 laser which is compliant to International Safety Standard EN 60825-1.

**Ordering Information**

<i>PART NUMBER</i>	<i>INPUT/OUTPUT</i>	<i>SIGNAL DETECT</i>	<i>VOLTAGE</i>	<i>TEMPERATURE</i>
TS3-0155-34S-P1	DC/DC	LVPECL	3.3 V	0°C to 70°C
TS3-0155-34S-P1-E	DC/DC	LVPECL	3.3 V	-40°C to 85°C

**Absolute Maximum Ratings**

<i>PARAMETER</i>	<i>SYMBOL</i>	<i>MIN</i>	<i>MAX</i>	<i>UNITS</i>	<i>NOTE</i>
Storage Temperature	$T_S$	-40	85	°C	
Supply Voltage	$V_{CC}$	-0.5	6.0	V	
Input Voltage	$V_{IN}$	-0.5	$V_{CC}$	V	
Output Current	$I_o$	---	50	mA	
Operating Current	$I_{OP}$	---	400	mA	
Soldering Temperature	$T_{SOLD}$	---	260	°C	10 seconds on leads

**1310 nm Single-mode Transceiver (S1.1)**  
**2×5, LC Duplex Connector, 3.3 V**  
**155 Mbps ATM/SONET OC-3/SDH STM-1**



**Operating Environment**

<i>PARAMETER</i>	<i>SYMBOL</i>	<i>MIN</i>	<i>MAX</i>	<i>UNITS</i>	<i>NOTE</i>
Ambient Operating Temperature (TS3-0155-34S-P1)	$T_{AMB}$	0	70	°C	
Ambient Operating Temperature (TS3-0155-34S-P1-E)	$T_{AMB}$	-40	85	°C	
Supply Voltage	$V_{CC}$	3.1	3.5	V	

**Transmitter Electro-optical Characteristics**

( $V_{CC} = 3.1 \text{ V to } 3.5 \text{ V}$ , TS3-0155-34S-P1:  $T_A = 0^\circ \text{C to } 70^\circ \text{C}$ , TS3-0155-34S-P1-E:  $T_A = -40^\circ \text{C to } 85^\circ \text{C}$ )

<i>PARAMETER</i>	<i>SYMBOL</i>	<i>MIN</i>	<i>TYP.</i>	<i>MAX</i>	<i>UNITS</i>	<i>NOTE</i>
Data Rate	$B$	50	155	200	Mb/s	
Output Optical Power 9/125 $\mu\text{m}$ fiber	$P_{out}$	-15	---	-8	dBm	Average
Extinction Ratio	$ER$	8.2	---	---	dB	
Center Wavelength	$\lambda_C$	1261	1310	1360	nm	
Spectral Width (RMS)	$\Delta\lambda$	---	---	4	nm	
Rise/Fall Time (10–90%)	$T_{r,f}$	---	1	2	ns	
Output Eye	Compliant with Telcordia GR-253-CORE Issue 3 and ITU-T recommendation G-957					
Power Supply Current	$I_{CC}$	---	---	140	mA	Note 1
Data Input Current-Low	$I_{IL}$	-350	---	---	$\mu\text{A}$	
Data Input Current-High	$I_{IH}$	---	---	350	$\mu\text{A}$	
Disable input voltage-High	$T_{dis}$	2.2	---	---	V	
Disable input voltage-Low	$T_{dis}$	---	---	0.6	V	
Transmitter Data Input Voltage-High	$V_{IH} - V_{CC}$	-1.1	---	-0.74	V	Note 2
Transmitter Data Input Voltage-Low	$V_{IL} - V_{CC}$	-2.0	---	-1.58	V	Note 2

Note 1: Not including the terminations.

Note 2: These inputs are compatible with 10K, 10KH and 100K ECL and PECL input.



http://www.ritekom.com

Version 2.1

Headquarters :  
 No. 669, Sec. 4 Chung Hsing Road  
 Chudung, HsinChu, Taiwan 310, ROC  
 Tel: +886-3-582-8270  
 Fax: +886-3-582-8416

North American Office:  
 1065 E. Hillsdale Blvd., Suite 215,  
 Foster City, CA 94404, USA  
 Tel: +1 (650) 578-1378  
 Fax: +1 (650) 578-9678

**1310 nm Single-mode Transceiver (S1.1)**  
**2×5, LC Duplex Connector, 3.3 V**  
**155 Mbps ATM/SONET OC-3/SDH STM-1**



**Receiver Electro-optical Characteristics**

( $V_{CC} = 3.1 \text{ V to } 3.5 \text{ V}$ , TS3-0155-34S-P1:  $T_A = 0^\circ \text{C to } 70^\circ \text{C}$ , TS3-0155-34S-P1-E:  $T_A = -40^\circ \text{C to } 85^\circ \text{C}$ )

PARAMETER	SYMBOL	MIN	TYP.	MAX	UNITS	NOTE
Data Rate	$B$	50	155	200	Mb/s	
Optical Input Power -maximum	$P_{IN}$	0	---	---	dBm	Note 1
Optical Input Power -minimum (Sensitivity)	$P_{IN}$	---	---	-34	dBm	Note 1
Operating Center Wavelength	$\lambda_C$	1100	---	1600	nm	
Signal Detect-Asserted	$P_A$	---	---	-34	dBm	Average
Signal Detect-Deasserted	$P_D$	-47	---	---	dBm	Average
Signal Detect-Hysteresis	$P_A - P_D$	1.0	---	---	dB	
Signal Detect Assert Time	$T_{SD+}$	---	---	100	$\mu\text{s}$	
Signal Detect Deassert Time	$T_{SD-}$	---	---	100	$\mu\text{s}$	
Signal Detect Output voltage - High	$V_{OH} - V_{CC}$	-1.1	---	-0.74	V	Note 2
Signal Detect Output voltage - Low	$V_{OL} - V_{CC}$	-2.0	---	-1.58	V	Note 2
Power Supply Current	$I_{CC}$	---	---	100	mA	Note 3
Data Output Rise, Fall Time (10%~90%)	$T_{r,f}$	---	1	2	ns	
Data Output Voltage-High	$V_{OH} - V_{CC}$	-1.1	---	-0.74	V	Note 2
Data Output Voltage-Low	$V_{OL} - V_{CC}$	-2.0	---	-1.58	V	Note 2

Note 1: The input data is at 155.52 Mbps,  $2^{23}-1$  PRBS data pattern with 72 "1"s and 72 "0"s inserted per the ITU-T recommendation G958 Appendix 1. The receiver is guaranteed to provide output data with Bit Error Rate (BER) better than or equal to  $1 \times 10^{-10}$ .

Note 2: These outputs are compatible with 10K, 10KH and 100K ECL and PECL input.

Note 3: The current excludes the output load current.



<http://www.ritekom.com>

Version 2.1

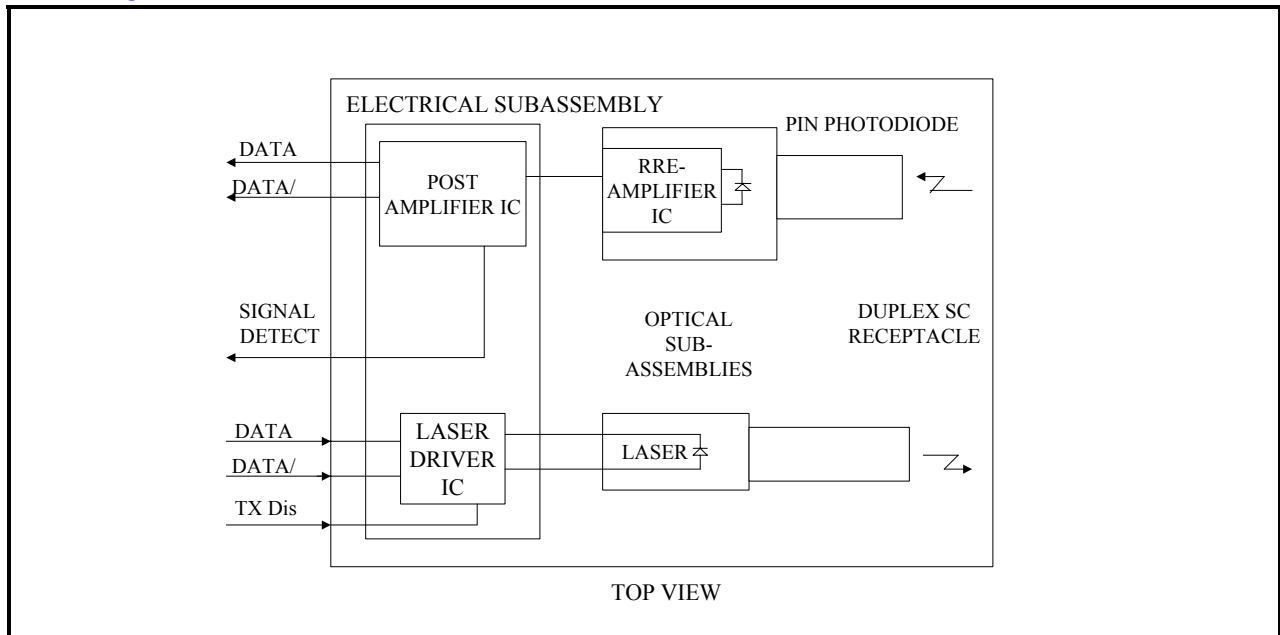
Headquarters :  
 No. 669, Sec. 4 Chung Hsing Road  
 Chudung, HsinChu, Taiwan 310, ROC  
 Tel: +886-3-582-8270  
 Fax: +886-3-582-8416

North American Office:  
 1065 E. Hillsdale Blvd., Suite 215,  
 Foster City, CA 94404, USA  
 Tel: +1 (650) 578-1378  
 Fax: +1 (650) 578-9678

**1310 nm Single-mode Transceiver (S1.1)**  
**2×5, LC Duplex Connector, 3.3 V**  
**155 Mbps ATM/SONET OC-3/SDH STM-1**



**Block Diagram of Transceiver**



**Transmitter Section**

The transmitter section consists of a 1310 nm InGaAsP laser in an eye safe optical subassembly (OSA) which mates to the fiber cable. The laser OSA is driven by a LD driver IC which converts differential input LVPECL logic signals into an analog laser driving current.

**Transmitter Disable**

Transmitter Disable is a TTL control pin. To disable the module, connect this pin to +3.3 V TTL logic high "1". While, to enable module connect to TTL logic low "0".

**Receiver Section**

The receiver utilizes an InGaAs PIN photodiode mounted together with a trans-impedance preamplifier IC in an OSA. This OSA is connected to a circuit providing post-amplification quantization, and optical signal detection.

**Receiver Signal Detect**

Signal Detect is a basic fiber failure indicator. This is a single-ended LVPECL output. As the input optical power is decreased, Signal Detect will switch from high to low (deassert point) somewhere between sensitivity and the no light input level. As the input optical power is increased from very low levels, Signal Detect will switch back from low to high (assert point). The assert level will be at least 1.0 dB higher than the deassert level.



[http:// www.ritekom.com](http://www.ritekom.com)

Version 2.1

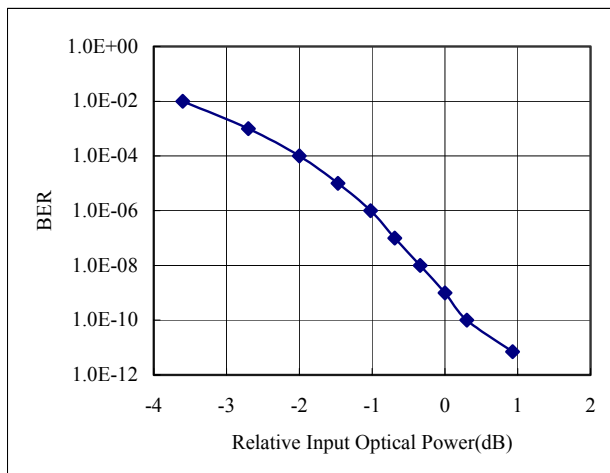
Headquarters :  
No. 669, Sec. 4 Chung Hsing Road  
Chudung, HsinChu, Taiwan 310, ROC  
Tel: +886-3-582-8270  
Fax: +886-3-582-8416

North American Office:  
1065 E. Hillsdale Blvd., Suite 215,  
Foster City, CA 94404, USA  
Tel: +1 (650) 578-1378  
Fax: +1 (650) 578-9678

**1310 nm Single-mode Transceiver (S1.1)**  
**2×5, LC Duplex Connector, 3.3 V**  
**155 Mbps ATM/SONET OC-3/SDH STM-1**



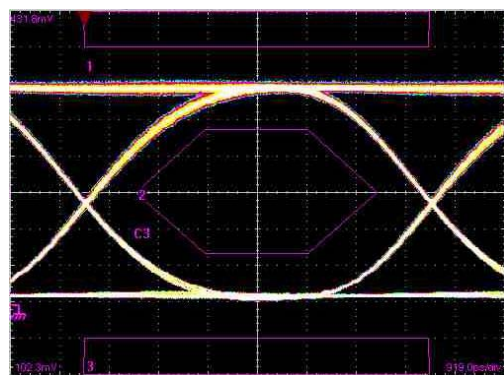
**Typical BER Performance of Receiver versus Input Optical Power Level**



The figure shows the relationship between typical trade-off of BER and Relative Input Optical Power. Besides the required BER =  $1 \times 10^{-10}$  of the ATM Forum 155.52 Mbps Physical Layer Standard, The transceiver can be operated at other Bit-Error-Rate conditions. The Relative Input Optical Power in dB is referenced to the actual sensitivity of the device. For BER conditions better than  $1 \times 10^{-10}$ , more input signal is needed (+dB).

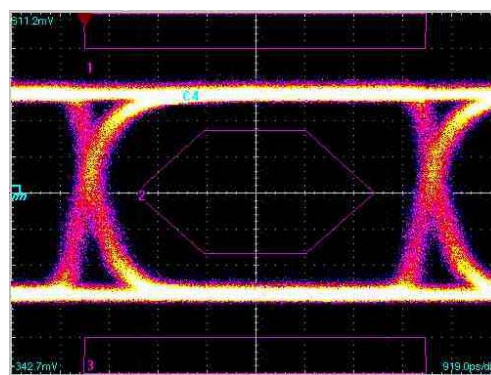
**Eye Diagram**

Transmitter



Signal pattern: PRBS 23

Receiver



Signal pattern: PRBS 23  
 Input Power: -35 dBm



[http:// www.ritekom.com](http://www.ritekom.com)

Version 2.1

Headquarters :  
 No. 669, Sec. 4 Chung Hsing Road  
 Chudung, HsinChu, Taiwan 310, ROC  
 Tel: +886-3-582-8270  
 Fax: +886-3-582-8416

5/10

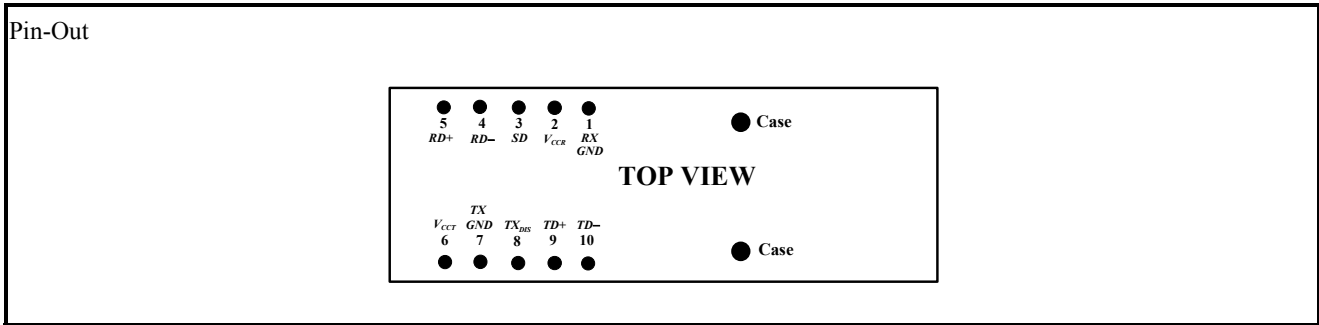
North American Office:  
 1065 E. Hillsdale Blvd., Suite 215,  
 Foster City, CA 94404, USA  
 Tel: +1 (650) 578-1378  
 Fax: +1 (650) 578-9678

Date : 2002/03/18

**1310 nm Single-mode Transceiver (S1.1)**  
**2×5, LC Duplex Connector, 3.3 V**  
**155 Mbps ATM/SONET OC-3/SDH STM-1**



**Connection Diagram**



<i>PIN</i>	<i>SYMBOL</i>	<i>DESCRIPTION</i>
1	<i>RX GND</i>	Receiver Signal Ground. Directly connect this pin to the receiver ground plane.
2	<i>VCCR</i>	Receiver Power Supply Provide +3.3 Vdc via the recommended receiver power supply filter circuit. Locate the power supply filter circuit as close as possible to the <i>VCCR</i> pin.
3	<i>SD</i>	Signal Detect. Normal optical input levels to the receiver result in a logic “1” output, <i>V<sub>OH</sub></i> , asserted. Low input optical levels to the receiver result in a fault condition indicated by a logic “0” output <i>V<sub>OL</sub></i> , deasserted. Signal Detect is a single-ended LVPECL output. <i>SD</i> can be terminated with LVPECL techniques via 50Ω to <i>VCCR</i> – 2 V. Alternatively, <i>SD</i> can be loaded with a 180 Ω resistor to <i>RX GND</i> to conserve electrical power with small compromise to signal quality. If Signal Detect output is not used, leave it open-circuited. This Signal Detect output can be used to drive a LVPECL input on an upstream circuit, such as, Signal Detect input or Loss of Signal-bar.
4	<i>RD-</i>	<i>RD-</i> is an open-emitter output circuit. Terminate this high-speed differential LVPECL output with standard LVPECL techniques at the follow-on device input pin. (See recommended circuit schematic)
5	<i>RD+</i>	<i>RD+</i> is an open-emitter output circuit. Terminate this high-speed differential LVPECL output with standard LVPECL techniques at the follow-on device input pin. (See recommended circuit schematic)
6	<i>VCCR</i>	Transmitter Power Supply. Provide +3.3 Vdc via the recommended transmitter power supply filter circuit. Locate the power supply filter circuit as close as possible to the <i>VCCR</i> pin.
7	<i>TX GND</i>	Transmitter Signal Ground. Directly connect this pin to the transmitter signal ground plane. Directly connect this pin to the transmitter ground plane.
8	<i>TXDIS</i>	Transmitter Disable. Connect this pin to +3.3V TTL logic high “1” to disable transmitter. To enable module connect to TTL logic low “0” or open.
9	<i>TD+</i>	Transmitter Data In. Terminate this high-speed differential LVPECL input with standard LVPECL techniques at the transmitter input pin. (See recommended circuit schematic)
10	<i>TD-</i>	Transmitter Data In-Bar. Terminate this high-speed differential LVPECL input with standard LVPECL techniques at the transmitter input pin. (See recommended circuit schematic)



http:// www.ritekom.com

Version 2.1

Headquarters :  
 No. 669, Sec. 4 Chung Hsing Road  
 Chudung, HsinChu, Taiwan 310, ROC  
 Tel: +886-3-582-8270  
 Fax: +886-3-582-8416

North American Office:  
 1065 E. Hillsdale Blvd., Suite 215,  
 Foster City, CA 94404, USA  
 Tel: +1 (650) 578-1378  
 Fax: +1 (650) 578-9678

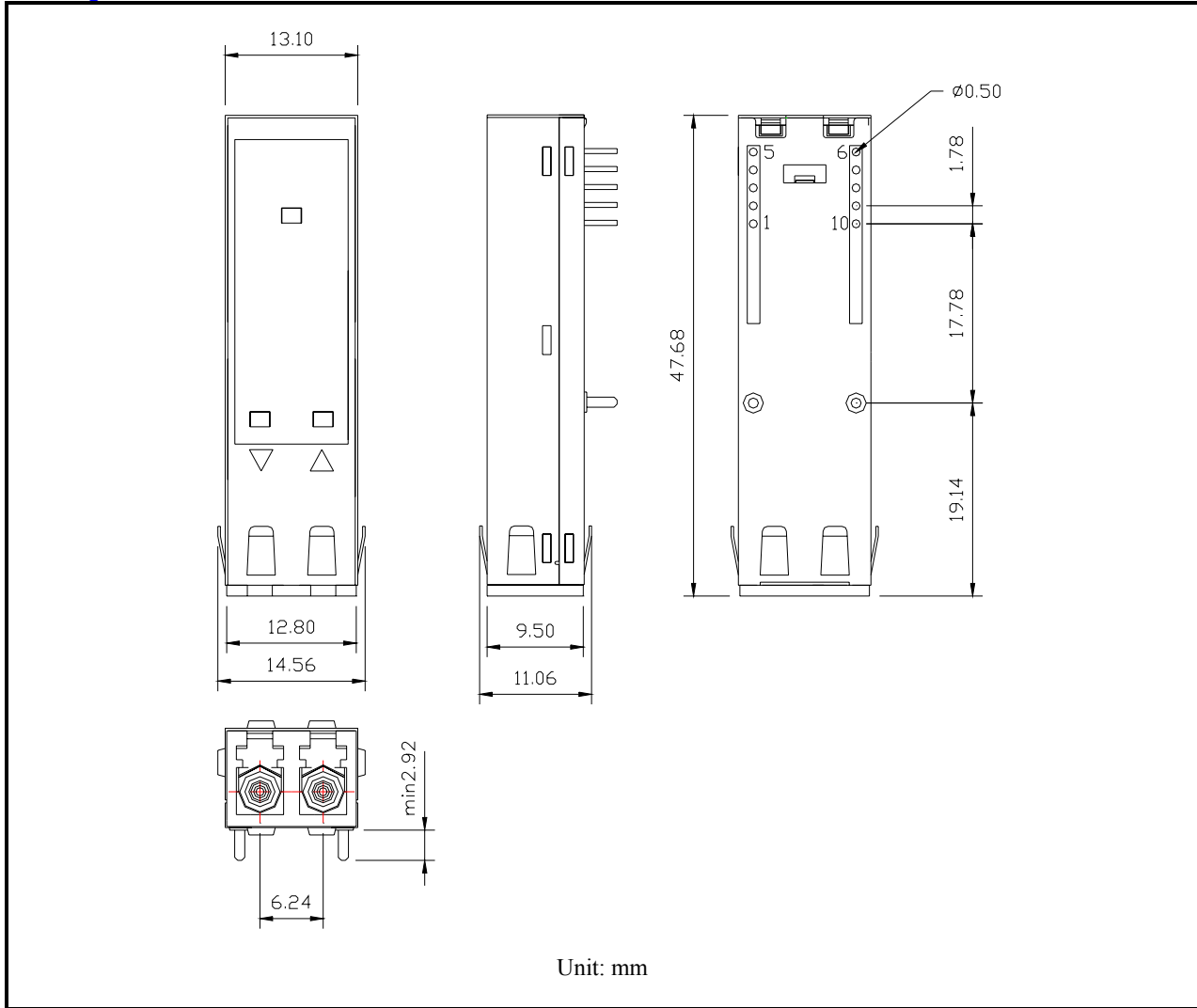


**1310 nm Single-mode Transceiver (S1.1)**  
**2×5, LC Duplex Connector, 3.3 V**  
**155 Mbps ATM/SONET OC-3/SDH STM-1**



for the power supply currents.

**Drawing Dimensions**



[http:// www.ritekom.com](http://www.ritekom.com)

Version 2.1

Headquarters :  
 No. 669, Sec. 4 Chung Hsing Road  
 Chudung, HsinChu, Taiwan 310, ROC  
 Tel: +886-3-582-8270  
 Fax: +886-3-582-8416

8/10

North American Office:  
 1065 E. Hillsdale Blvd., Suite 215,  
 Foster City, CA 94404, USA  
 Tel: +1 (650) 578-1378  
 Fax: +1 (650) 578-9678

Date : 2002/03/18






**1310 nm Single-mode Transceiver (S1.1)**  
**2x5, LC Duplex Connector, 3.3 V**  
**155 Mbps ATM/SONET OC-3/SDH STM-1**



**Regulatory Compliance**

<i>FEATURE</i>	<i>TEST METHOD</i>	<i>PERFORMANCE</i>
Electrostatic discharge (ESD) to the electrical pins	MIL-STD-883D Method 3015.7	Class 1(>1 kV) – Human Body Model
Electromagnetic interference (EMI)	FCC Class B EN55022 Class B	The transceiver is mounded on a circuit card without a chassis enclosure at frequencies up to 1000 MHz. Margins will be dependent on customer's board and chassis designs.
Immunity	Variation of IEC801-3	Typically show no measurable effect from a 10 V/m filed swept from 30 MHz to 1000 MHz applied to the transceiver without a chassis enclosure.
Eye safety	FDA 21 CFR 1040.10 and 1040.11 Class 1	FDA Accession Number: 0012715-01
	EN 60825-1:1994+A11 EN 60950: 1992+A1+A2+A3+A4+A11 EN 60825-2: 1994+A1	TUV certificated Number: R 3-50005227

**Eye Safety Mark**

<p>The TS3 series Singlemode transceiver is a class 1 laser product. It complies with EN 60825-1 and FDA 21 CFR 1040.10 and 1040.11. In order to meet laser safety requirements the transceiver shall be operated within the Absolute Maximum Ratings.</p> <p><b>Caution</b>  <b>All adjustments have been done at the factory before the shipment of the devices. No maintenance and user serviceable part is required. Tampering with and modifying the performance of the device will result in voided product warranty.</b></p>	<p><b>Required Mark</b></p> <div style="border: 1px solid black; padding: 5px; text-align: center;"> <p><b>Class 1 Laser Product Complies with 21 CFR 1040.10 and 1040.11</b></p> </div> <p><b>Laser Emission</b></p> <p>Indication of LASER Aperture and Beam ← </p>
---	--



emitec ag, hinterbergstrasse 9, 6330 Cham  
tel 041 / 748 60 10, fax 041/ 748 60 11  
info@emitec.ch, www.emitec.ch

North American Office:  
1065 E. Hillsdale Blvd., Suite 215,  
Foster City, CA 94404, USA  
Tel: +1 (650) 578-1378  
Fax: +1 (650) 578-9678